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UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No.

35.C14089

First Named Inventor or Application Identifier

TAIKO MOTOI ET AL.

Express Mail Label No.

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

ADDRESS TO:

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1. ☐ Fee Transmittal Form
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2. ☒ Specification Total Pages

3. ☒ Drawing(s) (35 USC 113) Total Sheets

4. ☒ Oath or Declaration Total Pages

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c. ☐ Copy from a prior application (37 CFR 1.63(d))
(for continuation/divisional with Box 17 completed)
[Note Box 5 below]

i. ☐ **DELETION OF INVENTOR(S)**
Signed Statement attached deleting
inventor(s) named in the prior application, see
37 CFR 1.63(d)(2) and 1.33(b).

5. ☐ Incorporation By Reference (useable if Box 4c is checked)
The entire disclosure of the prior application, from which a copy of
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ACCOMPANYING APPLICATION PARTS

8. ☐ Assignment Papers (cover sheet & document(s))

9. ☐ 37 CFR 3.73(b) Statement (when there is an assignee) ☐ Power of Attorney

10. ☐ English Translation Document (if applicable)

11. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations

12. ☐ Preliminary Amendment

13. ☒ Return Receipt Postcard (MPEP 503)
(Should be specifically itemized)

14. ☐ Small Entity Statement(s) ☐ Statement filed in prior application
Status still proper and desired

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CLAIMS	(1) FOR	(2) NUMBER FILED	(3) NUMBER EXTRA	(4) RATE	(5) CALCULATIONS
	TOTAL CLAIMS (37 CFR 1.16(c))	41-20 =	21	X \$ 18.00 =	\$ 378.00
	INDEPENDENT CLAIMS (37 cfr 1.16(b))	5-3 =	2	X \$ 78.00 =	\$ 156.00
	MULTIPLE DEPENDENT CLAIMS (if applicable) (37 CFR 1.16(d))			\$260.00 =	\$ 260.00
				BASIC FEE (37 CFR 1.16(a))	\$ 760.00
				Total of above Calculations =	\$1,554.00
	Reduction by 50% for filing by small entity (Note 37 CFR 1.9, 1.27, 1.28).				
	TOTAL =				\$1,554.00

19. Small entity status

- a. ☐ A Small entity statement is enclosed
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- c. ☐ Is no longer claimed.

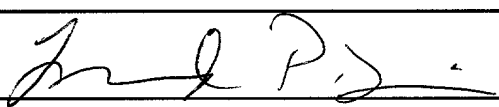
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- a. ☒ Fees required under 37 CFR 1.16.
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED

NAME	LEONARD P. DIANA, Reg. No. 29,296
SIGNATURE	
DATE	November 18, 1999

ELECTRON-EMITTING DEVICE, ELECTRON SOURCE USING
ELECTRON-EMITTING DEVICE, AND IMAGE FORMING APPARATUS

BACKGROUND OF THE INVENTION

5 Field of the Invention

This invention relates to an electron-emitting device, an electron source using the electron-emitting device, and an image forming apparatus.

Related Background Art

10 Conventionally, as an electron-emitting device, generally two kinds respectively using a thermionic cathode and a cold cathode are known. As the cold cathode, there is a field emission type (hereinafter referred to as an FE type), a metal/insulation
15 layer/metal type (hereinafter referred to as an MIM type), a surface conduction type electron-emitting device or the like. As examples of the FE type, those which have been disclosed in W.P. Dyke & W.W.Dolan, "Field emission", Advance in Electron Physics, 8,89
20 (1956) or C.A. Spindt. "Physical Properties of thin-film field emission cathodes with molybdenum cones", J. Appl. Phys., 47.5248 (1976), etc. are known.

As examples of the MIM type, those which are disclosed in C.A. Mead", Operation of Tunnel-Emission
25 Devices", J Apply. Phys. 32, 646 (1961), etc. are known.

As examples for the surface conduction type

electron-emitting device, there are those which have been disclosed in M.I. Elinson, Radio Eng. Electron Phys, 10, 1290, (1965), etc.

5 The surface conduction type electron-emitting device is to utilize phenomena giving rise to the electron emission by making a current flow in parallel with the film surface at a small area of a film formed on a substrate. For this surface conduction type electron-emitting device, the one utilizing SnO_2 film by
10 aforementioned Elinson et al., the one involving Au film (G. Dittmer, Thin Solid Films, 9.317(1972)), the one involving $\text{In}_2\text{O}_3/\text{SnO}_2$ film (M. Hartwell and C. G. Fonsted, IEEE Trans. ED Conf., 519 (1975)), and the one involving carbon film (Hisashi Araki, et al., Vacuum,
15 vol. 26, the first issue, page 22 (1983)), etc. have been reported.

 The present applicant has presented a number of proposals on surface conduction type electron-emitting devices having novel configurations and their
20 applications. Its basic configuration and manufacturing method, etc. have been disclosed in for example Japanese Patent Application Laid-Open No. 7-235255, Japanese Patent No. 2836015, Japanese Patent No. 2903295, etc.

25 Now, their points are briefly described below.

 An example of surface conduction type electron-emitting device disclosed in the above-described

publication is schematically shown in FIGS. 5A and 5B.
As in FIGS. 5A and 5B, the device is configured to
comprise a pair of device electrodes 2 and 3 facing
each other on the substrate 1, and conductive film 4
5 which is connected with the device electrodes and has
an electron-emitting region 5 in a part thereof. FIG.
5A is its schematic plan view, and FIG. 5B is its
schematic sectional view. The electron-emitting region
5 is a portion where a part of the conductive film 4
10 has been destroyed, deformed, or changed in quality.
And the electron-emitting region has a fissure. On the
substrate 1 inside the fissure and on its adjacent
conductive film 4, the deposit comprising carbon and/or
carbon compound as main ingredients has been formed
15 with a step called activation process.

SUMMARY OF THE INVENTION

As for the surface conduction type electron-
emitting device, further stable and long-lasting
20 electron emission characteristics are desired so that
the applied image forming apparatus can provide bright
on-screen images on stable basis for a long period. If
the electron emission characteristics controllable on
stable basis, improvement of efficiency and long life
25 are achieved, in for example an image forming apparatus
comprising fluorescent substance as an image forming
member, a low-power (low-voltage, low-current), bright

and high definition image forming apparatus, for example a flat television, can be obtained. In an image forming apparatus, electrons emitted from an electron-emitting device reach a face plate being an anode to which a voltage of several kV has been applied, and lighten the fluorescent substance on the face plate to radiate.

However, a composition of the aforementioned carbon containing film (carbon film) could give rise to chemical changes due to the atmosphere surrounding the device or the like, or vaporize due to heat generated at the time of driving or various heating processes, etc. And, such chemical changes and vaporization could result in unstable or deteriorated electron emission characteristics.

Moreover, when the aforementioned vaporization takes place during driving pressure surrounding the device increases locally. Thus, discharge, etc. presumably due to the aforementioned vaporized substance could destroy conductive films or electrodes to give rise to a rapid deterioration of electron emission characteristics.

In addition, in the electron source in which the devices accompanied by the aforementioned vaporization are densely arranged, the distance among adjacent devices is short. Therefore, it is anticipated that the vaporized substance generated from one device could

affect adjacent devices as well. As a result, in addition to that phenomena such as unstableness and deterioration of devices, and discharge, etc., become remarkable, decrease in uniformity of electron source
5 or decrease in the on-screen image definition of an image forming apparatus could take place.

Under the circumstance, the purpose of the present invention is to obtain an electron-emitting device having a chemically and thermally stable carbon film
10 thereby to obtain an electron-emitting device having over a long period stable electron emission characteristics and excellent electron emission efficiency. In addition, another purpose hereof is to obtain an electron source having excellent electron
15 emission efficiency, and electron emission characteristics highly uniform over a long period. Further another purpose hereof is to obtain an image forming apparatus capable of controlling change and deterioration in the aforementioned electron emission
20 characteristics and thereby obtaining highly uniform image over a long time.

Under the circumstances, as a result of a study contemplating on the above-described problems, the electron-emitting device of the present invention
25 comprises a substrate, a first and a second carbon film having a first gap between them disposed on the surface of the substrate, and a first and a second electrode

respectively electrically connected with the first and the second carbon film, wherein

the carbon film has a region showing orientation, and the direction of the orientation is approximately parallel to the substrate surface.

The electron-emitting device of the present invention also comprises, a substrate,

a first and a second electrode respectively having disposed on the substrate surface,

a first and a second conductive film having a second gap disposed between the electrodes and respectively connected with the aforementioned and the second electrode,

a first and a second carbon film having a first gap within the second gap and disposed so as to be respectively connected with the first and the second conductive film, wherein

the first and the second carbon film respectively covers a part of the first and the second conductive film,

and the carbon film disposed on the substrate surface has a region showing orientation, and a direction of the orientation is approximately normal direction to the substrate surface.

The electron-emitting device of the present invention also comprises a region where the carbon film does not show a particular orientation, wherein the

region not showing a particular orientation is disposed between the region having orientation in the approximately parallel direction to the substrate surface and the region having orientation in the approximately normal direction to the substrate surface.

The present invention is further characterized by an electron source in which a plurality of the above-mentioned electron-emitting devices are arranged on the substrate, and is further characterized by an image forming apparatus having the above-mentioned electron source and an image forming member.

In the electron-emitting device of the present invention, excellent efficiency can be obtained on stable basis over a long period. In addition, in the electron source of the present invention, the electron emission characteristics excellent in uniformity and stable over a long period can be obtained. Moreover, in the image forming apparatus of the present invention, on-screen images excellent in uniformity can be obtained on stable basis over a long period.

BRIEF DESCRIPTION OF THE DRAWINGS

FIGS. 1A, 1B and 1C are a schematic plan view and sectional views showing a configuration of an electron-emitting device of the present invention;

FIGS. 2A, 2B, 2C and 2D are schematic diagrams

showing a part of manufacturing process of an electron-emitting device of the present invention;

FIG. 3 is a schematic diagram showing an example of configuration of a vacuum processing system provided with measurement-evaluation function;

FIGS. 4A and 4B are schematic diagrams showing an example of voltage wave form available for use in the forming step being a part of manufacturing step of the electron-emitting device of the present invention;

FIGS. 5A and 5B are a schematic plan view and a sectional view showing a configuration of a conventional electron-emitting device;

FIGS. 6A and 6B are schematic diagrams showing an example of fluorescent film;

FIG. 7 is a schematic diagram showing relationships between the emission current I_e and the device voltage V_f and between the device current I_f and the device voltage V_f , of an electron-emitting device of the present invention;

FIG. 8 is a schematic diagram showing an example in which electron-emitting devices of the present invention have been applied to the electron sources disposed in a matrix formation;

FIG. 9 is a schematic diagram showing an example in which an electron-emitting device of the present invention has been applied to an image forming apparatus;

FIG. 10 is a schematic diagram showing an example of a vacuum processing system being used in the manufacturing step of an image forming apparatus at the time when an electron-emitting device of the present invention has been applied to the image forming apparatus;

FIG. 11 is a schematic diagram showing an example in which electron-emitting devices of the present invention have been applied to the electron sources disposed in a ladder formation;

FIG. 12 is a schematic diagram showing another example in which an electron-emitting device of the present invention has been applied to an image forming apparatus;

FIGS. 13A and 13B are schematic diagrams showing examples of voltage wave forms available for use in the activation step as a part of the manufacturing step of electron-emitting device of the present invention;

FIG. 14 is a schematic diagram showing an example in which electron-emitting devices of the present invention have been applied to electron sources disposed in a matrix formation;

FIG. 15 is a partial sectional schematic diagram along the broken line 15 - 15 in FIG. 14;

FIGS. 16A, 16B, 16C and 16D are schematic diagrams to describe a part of manufacturing step of an electron-emitting device related to the examples of the

present invention;

FIGS. 17E, 17F and 17G are schematic diagrams to describe a part of manufacturing step of an electron source related to the examples of the present

5 invention;

FIGS. 18A and 18B are a schematic diagram showing lattice fringes (lattice image) and orientation thereof in a region adjacent gap portion 6 of the film containing carbon of the present invention;

10 FIGS. 19A and 19B are a schematic diagram showing lattice fringes (lattice image) and orientation thereof in a region apart from the gap portion 6 of the film containing carbon of the present invention;

15 FIG. 20 is a schematic diagram showing lattice fringes (lattice image) and orientation thereof in a region between a region adjacent the gap portion 6 of the film containing carbon of the present invention and a region apart from the gap portion 6;

20 FIG. 21 is a schematic diagram showing another mode of electron-emitting device of the present invention; and

FIGS. 22A and 22B are schematic diagrams showing another mode of electron-emitting device of the present invention.

25

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Now, with reference to the drawings the present

invention will be described in detail.

FIGS. 1A and 1B are a plan view and a sectional view representing as a schematic diagram a planar type electron-emitting device of the present invention. A pair of electrodes 2 and 3 are disposed facing each other on a substrate 1. A second gap 6 formed in a part of a conductive film 4 by the later-described forming step, etc. The conductive films 4 are facing each other substantially parallel to the surface of the substrate 1. And, the conductive film 4 covers for example the surface of the electrodes 2 and 3 as shown in FIGS. 2A to 2D, and thus a pair of electrodes and the conductive film are electrically connected. Connection between the conductive film 4 and the electrodes 2 and 3 may be disposed in such a manner that the electrodes 2 and 3 are disposed on the conductive film 4 and the like without being limited to the mode shown in FIGS. 2A to 2D. Incidentally, as shown in FIGS. 1A and 1B, the conductive film 4 is separated left and right with the gap 6 as a center to be disposed facing each other, but in some cases could remain not-perfectly separated at one part in the second gap 6.

Moreover, the later-described activation step disposes a film comprising carbon (carbon film) 10 on the substrate 1 within the second gap 6 and on the adjacent conductive film 4.

The film comprising carbon (carbon film) 10 is disposed facing each other substantially parallel to the surface of the substrate 1 over the first gap 7 as a center disposed within the second gap 6.

5 This film comprising carbon 10 can cover to reach above the device electrodes 2 and 3 as shown in FIGS. 22A and 22B, depending on distance between electrodes (L) and later-described activation conditions, etc., and moreover, without using the conductive film 4, the
10 electrodes 2 and 3 can be connected directly to the carbon films 10. Although details are described later, the conductive film 4 is extraordinary thin film and thus is apt to thermal structural changes and
15 compositional changes such as aggregation (cohesion), etc., due to heat at the time of manufacturing process and at the time of driving. Therefore, in the present invention, in the case where the conductive film is
20 used, the above-described carbon film 10 covers the conductive film surface, preferably. And, especially, entire coverage of the conductive film surface located
25 between the electrodes 2 and 3 preferably controls variation in the device characteristics due to thermal structural changes of the conductive film, etc. In addition, in the case where the conductive film is not
used, the gap between the device electrodes is equivalent to the aforementioned second gap.

Incidentally, as shown in FIGS. 1A and 1B, the

film comprising carbon (carbon film) 10 is separated
left and right with the gap 7 as a center to be
disposed facing each other, but in some cases the film
comprising carbon (carbon film) 10 could remain not-
5 perfectly separated at one part in the first gap 7.

A voltage is applied between the electrodes 2 and
3 so that the electron-emitting device of the present
invention shown in FIGS. 1A to 1C configured as
described so far causes electrons to be emitted from
10 the electron-emitting region 5.

In addition, thickness of the film comprising
carbon 10 is preferably set within a range not less
than 5 nm and not more than 100 nm.

In the electron-emitting device of the present
15 invention, the carbon film 10 has particular
orientation. In other words, the carbon film has a
region showing the orientation of the carbon atoms.
Orientation in the present invention refers to a
direction to which lattice fringes (lattice image)
20 equivalent to graphite (002) plane (normal direction to
lattice fringes (lattice image)) are laminated.

And, for the above-described carbon film disposed
on at least the conductive film 4 (on the electrodes 2
and 3 for a mode without using a conductive film), the
25 lattice fringes (lattice image) equivalent to graphite
(002) plane are configured to have orientation in the
direction of approximate perpendicular against the

surface of the substrate, the sectional schematic diagram of which has been shown in FIG. 1C, 19A and 19B.

FIG. 19A is a sectional view having schematically shown the lattice fringes (lattice image) observed on the above-described conductive film 4, and the FIG. 19B is a sectional schematic diagram showing a part of FIG. 19A which has been magnified.

Incidentally, also in a mode without using the aforementioned conductive film 4, the lattice fringes (lattice image) observed in the carbon film on the electrodes 2 and 3 are basically the same as those shown in the schematic diagram of FIGS. 19A and 19B.

The carbon film 10 is, as described above, disposed in a state of an extremely thin film, and many regions thereof have been disposed on the aforementioned conductive film and/or on the aforementioned electrodes.

Thus, the above-described carbon film disposed on at least the conductive film 4 (on the electrodes 2 and 3 for a mode without using a conductive film) is adopted as the carbon film 10 which has orientation in the direction of approximate perpendicular against the surface of the substrate so that larger part of the carbon film being exposed in the atmosphere surrounding the device can be made thermally and chemically stable. As a result, various evaporation and chemical changes

from the film containing carbon due to heating step at the time when the electron-emitting device is driven or at the time of manufacturing an image forming apparatus and the like can be suppressed. Moreover, since
5 effects due to absorption of impurities and the like are reduced, electron emission characteristics stable over a long time can be obtained.

Incidentally, the direction of orientation of the lattice fringes (lattice image) falls within the range
10 of ± 30 degrees from the normal to the surface of the substrate having shown in FIGS. 19A and 19B. In addition, the direction of orientation of lattice fringes (lattice image) herein is referred to as a direction to which the lattice fringes (lattice image)
15 equivalent to graphite (002) plane are arranged in a lamination manner (normal direction to lattice fringes (lattice image)).

In addition, the lattice spacing of the above-described lattice fringes (lattice image) are
20 preferably comprised with those of not more than 4.7 \AA , and moreover, are further preferably comprised with those of not less than 3.5 \AA and not more than 4.7 \AA .

Moreover, the film containing carbon (carbon film)
10 of the present invention is preferably configured so
25 that lattice fringes (lattice image) (orientated direction) equivalent to graphite (002) plane are orientated in the substantially parallel direction to

the surface of the substrate 1.

The lattice fringes (lattice image) orientated in the parallel direction to the surface of the above-described substrate 1 are, as schematically shown in
5 FIGS. 1C, 18A and 18B, most preferably disposed in the vicinity of the first gap 7, that is, in the regions facing each other with the first gap 7 as a center.

FIG. 1C schematically shows sectional viewing of the lattice fringes (lattice image) of the film
10 containing carbon observed adjacent the gap 6 having shown in FIG. 1B.

The carbon film 10 of the portion facing the above-described first gap 7 is extremely thin, but has finite thickness, and is a portion forming the first
15 gap. Moreover, adjacent the above-described first gap is a region where largest quantity of heat is generated when the device is being driven, a region where strong electric fields are applied, and among others, a place where electrons are emitted. Therefore, it is
20 preferable that the region in the vicinity of the above-described first gap is chemically and thermally stable. That is, absorption of impurities, etc. which might take place on the surface of the carbon film in the portion which faces the first gap could give rise
25 to chemical compositional change, etc., and furthermore could give rise to a variation of work function. In addition, when reaction with atmosphere surrounding the

device results in vaporization of composed substance of carbon films, or heat results in evaporation of composed substance of carbon films, the shape of the first gap 7 might have changed. Consequently, it is possible that these result in variation and deterioration of electron emission characteristics.

Accordingly, the direction of the orientation of the carbon film 10 at the portion facing the first gap is in the approximately or substantially parallel to the surface of the substrate as described above, thus chemical stability and thermal stability can be obtained.

FIG. 18A is a sectional view on the lattice fringes (lattice image) in the vicinity of the first gap 7 having been shown in FIG. 1C, which have been magnified and schematically shown, and FIG. 18B is a schematic diagram showing the lattice spacing and the orientation of lattice fringes (lattice image).

As shown in FIG. 18B, the lattice fringes (lattice image) equivalent to the graphite (002) plane observed in the vicinity of the first gap 7 of the film comprising carbon (carbon film) 10 of the present invention have orientation in the approximately or substantially parallel to the surface of the substrate 1. The lattice fringes (lattice image) orientated to this direction are preferably disposed in the region of the distance of 100 nm from the end portion of the film

comprising carbon (carbon film) 10 regulating the first gap 7 toward the direction of the electrodes 2 and 3.

Incidentally, the orientation of lattice fringes (lattice image) falls within the range of ± 45 degrees from the substantially horizontal (parallel) line along the surface of the substrate having shown in FIG. 18B. In addition, the direction of orientation of lattice fringes (lattice image) herein is referred to as the direction to which the lattice fringes (lattice image) equivalent to graphite (002) plane are arranged in an overlapping manner (normal direction against lattice fringes (lattice image)).

In addition, the intervals of the lattice fringes (lattice image) orientated to the approximately or substantially parallel to the surface of the substrate 1 are preferably comprised with those of not more than 4.7 \AA , and moreover, are further preferably comprised with those of not less than 3.5 \AA and not more than 4.7 \AA .

Moreover, for a preferable mode of the carbon film 10 of the present invention, the carbon configuring the film comprising carbon (carbon film) 10 preferably has the configuration so that the lattice fringes (lattice image) equivalent to the graphite (002) plane does not show a particular orientated direction, as in FIG. 20 in which its sectional schematic diagram has been shown, in the region between the region where the

lattice fringes (lattice image) in the vicinity of the first gap 7 have orientation in the approximately parallel direction to the surface of the substrate and the region where the lattice fringes (lattice image) have orientation in the approximately normal direction to the surface of the substrate.

Since such a configuration makes the shape of the film comprising carbon structurally and also thermally stable in the region where orientation changes, an electron-emitting device having stable electron emission characteristics over a further long time can be obtained.

Here, the expression "do not show a particular orientated direction" includes those cases that the orientation, literally, cannot be specified by way of the later-described observation method, that in the direction of film thickness of the film comprising carbon (carbon film) 10 the orientation is directed in both ways defined to the aforementioned parallel direction and normal direction, and that the orientation includes the direction which does not fall within the range to be defined toward the above-described parallel direction and perpendicular direction.

As described so far, the most preferable mode of the film comprising carbon 10 of the present invention is configurations that the lattice fringes (lattice

image) in the vicinity of the first gap 7 are orientated to the substantially parallel direction to the surface of the substrate, and the lattice fringes (lattice image) remote from the first gap 7 are

5 orientated to the approximately normal direction to the surface of the substrate, and moreover the lattice fringes (lattice image) in the region which does not separate the both parties do not show a particular orientated direction (FIG. 1C). And as shown in FIG.
10 1C, it will become important from the point of view of safety of electron emission characteristics that the carbon film 10 having the above-described orientation has been disposed approximately symmetrically so as to sandwich the first gap 7.

15 Incidentally, FIG. 1C shows an example that the region (the region does not show a particular orientated direction) connecting the region where the lattice fringes (lattice image) in the vicinity of the first gap 7 are orientated in the parallel direction to
20 the surface of the substrate and the region where the lattice fringes (lattice image) remote from the first gap 7 are orientated in the approximately normal direction to the surface of the substrate are positioned on a substrate within the second gap 6.
25 However, as aforementioned, in the case where no conductive films are provided, or depending on the distance between electrodes or the interval of the

second gap, the region not showing a particular orientated direction could be located on the conductive film or electrodes.

5 The lattice stripe observed in the film comprising carbon (carbon film) 10 in the aforementioned present invention, and the orientation of lattice fringes (lattice image) and the intervals of lattice fringes (lattice image) are evaluated and observed as follows.

10 As an example of evaluation method, FIB (focused ion beam)-TEM (transparent electron magnifier) method are nominated, but the evaluation method is not limited to this method unless there is no inconvenience to evaluate the orientation of the film comprising carbon (carbon film).

15 In this evaluation method, FIB process has been used to produce samples for sectional TEM observation, and thus this pieces with thickness of not more than 100 nm can be produced in the region having length of several 10 μm so as to include the gaps 6 and 7, and it
20 is possible to evaluate with TEM the sections of the film comprising carbon 10 in the electron emission unit and in the vicinity thereof and surrounding it.

 Next, as concerns the evaluation method of orientation of the film comprising carbon 10 with TEM,
25 generally three methods are nominated as shown below.

 (1) A highly magnified TEM image of the film comprising carbon 10 is photographed and the lattice

fringes (lattice image) of the film comprising carbon 10 are observed. Here, the direction of orientation is given by the direction of lattice fringes (lattice image) and the lattice spacing is given from the distance between the fringes.

(2) The diffraction pattern obtainable when the micro probe is set onto the film comprising carbon 10 is photographed to measure distribution of intensity of diffraction ring. At this time, in the case when carbon 10 have an orientation, distribution of intensity of diffraction ring became heterogeneous, and the direction with stronger intensity of diffraction ring will become the orientation direction. In addition, the interval of lattice fringes is given by the distance between the position with the maximum intensity of diffraction ring and the origin of the diffraction pattern.

(3) The image obtained by photographing the lattice fringes of a highly magnified TEM image of the film comprising carbon 10 undergoes Fourier transform so that the diffraction pattern is obtained to measure distribution of intensity of diffraction ring. At this time, in the case when carbon 10 have an orientation, distribution of intensity of diffraction ring became heterogeneous, and the direction with stronger intensity of diffraction ring will become the orientation direction. In addition, the interval of

lattice fringes is given by the distance between the position with the maximum intensity of diffraction ring and the origin of the diffraction pattern.

Here, after obtaining the diffraction pattern as in (2) and (3), the intensity of orientation can also be converted into numeric values by way of comparing the intensity of diffraction ring in the orientated direction with the intensity of diffraction ring in the vertical direction to the oriented direction (for example, obtaining the intensity ratio).

However, the method described so far can be almost equivalent in principle and any method may be used for the evaluation of orientation without any inconveniences.

Next, an example of manufacturing method of the electron-emitting device of the present invention is described below. The step of forming the device electrodes and the conductive films, and the forming step, activation step is described briefly using FIGS. 2A to 2D.

1) The substrate 1 is sufficiently cleaned with detergent, pure water, and organic solvent, etc., and after the device electrode material is deposited with vacuum evaporation method, and sputtering method, etc., the device electrodes 2 and 3 are formed on the substrate 1 using for example photolithography technology (FIG. 2A).

Incidentally, as aforementioned, in the case where the film comprising carbon (carbon film) 10 is formed on the electrodes 2 and 3 without using the conductive film 4, the interval between the electrodes 2 and 3 may well set at around the second gap 6 to be formed with the later-described forming step using for example FIB method, etc., and in that case the following steps of 2) and 3) can be omitted. However, to form the device of the present invention on costly effective basis, it is preferable to form it with use of the above-described conductive film 4.

2) The substrate 1 has been provided with the device electrodes 2 and 3, to which, for example, organic metal compound solution is applied to form the organic metal compound film. In succession, the organic metal compound film undergoes baking and calcinating processing, and undergoes patterning by liftoff, and etching, etc., and the conductive film 4 is formed (FIG. 2B). Here, the application method of organic metal solution has been nominated for description, but the forming method of the conductive film 4 is not limited to this, but a vacuum evaporation method, sputtering method, chemical vapor depositing method, scattered application method, dipping method, spinner method, etc. can be used. In addition, a method of giving the aforementioned organic metal compound solution as liquid drops at desired positions

with an ink jet method can be used, and in this case the patterning step with liftoff or etching will become unnecessary.

Film thickness of the conductive film 4 is appropriately set putting step coverage to the electrodes 2 and 3, the resistance value of between the electrodes 2 and 3, and the later-described forming conditions, etc. under consideration, but normally, it will preferably fall within the range of several Å to several thousand Å, and more preferably from 10 Å to 500 Å. For those resistance values, R_s is a value of from $10^2 \Omega/\square$ to $10^7 \Omega/\square$. Incidentally, R_s emerges when resistance R of a film of thickness " t ", width " w ", and length l is set at $R=R_s(l/w)$. In the present applied specification, the forming processing is described taking conductive processing as an example, but the forming processing will not be limited to this, but will be inclusive of the processing to form the second gap 6 into the conductive film 4.

Materials consisting the conductive film 4 are appropriately selected from metals such as Pd, Pt, Ru, Ag, Au, Ti, In, Cu, Cr, Fe, Zn, Sn, Ta, W, and Pb, etc., oxide compound such as PdO, SnO_2 , In_2O_3 , PbO, Sb_2O_3 , etc., boron compound such as HfB_2 , ZrB_2 , LaB_6 , CeB_6 , YB_4 , GdB_4 , etc., carbon compound such as TiC, ZrC, HfC, TaC, SiC, WC, nitrogen compound such as TiN, ZrN, HfN, etc., and semiconductors such as Si, Ge, etc. and

the like.

3) In succession, forming step is implemented. As an example of step of this forming method, the method by way of conductive processing is explained. The
5 above-described electron-emitting device having formed the conductive film 4 is disposed in the vacuum apparatus, and the interior atmosphere is exhausted so as to get a pressure of for example 1×10^{-5} Torr and the like, and not-shown power source is used between
10 the electrodes 2 and 3 so as to applying voltage, then the second gap 6 is formed in the conductive film 4 (FIG. 2C).

As the voltage wave form to be used for the above-described forming process, pulse wave forms are
15 preferable. This includes technique to apply pulse with pulse wave height value of a constant voltage on continuous basis as having shown in FIG. 4A, and technique to apply voltage pulses while increasing pulse wave height value as having shown in FIG. 4B.

20 T1 and T2 in FIG. 4A is the pulse width and the pulse interval of a voltage wave form. Normally T1 is from 1 μ sec to 10 msec, and T2 is set to fall within the range from 10 μ sec to several 100 msec. Under such conditions, voltage is applied for the period of for
25 example from several seconds to several ten minutes. The pulse wave from is not limited to triangular wave, but desired wave forms such as rectangular wave can be

adopted.

T1 and T2 in FIG. 4B may be those shown in FIG. 4A. In addition, wave height value of triangular wave may be increased at a desired rate, for example, approximately every 0.1 V step.

The conclusion of the forming processing is determined by, for example, inserting pulse voltage between the pulse voltages for above-described forming process to an extent which will not locally destroy nor deform the conductive film 4, and measuring the current at that time to detect the resistant value. For example, measuring the device current which flows when a voltage around 0.1V is applied and obtaining the resistance values, and when resistant not less than 1,000 times as large as a resistance before the forming processing is indicated, forming process is concluded.

Incidentally, as the method of forming process, other than the above-described methods, any method which form the second gap 6 appropriately can be adopted.

4) Next, the activation step is implemented. For example, the activation step of the present invention is a step where under the atmosphere containing gas of acrylonitrile a pulse voltage is repeatedly applied to between the above-described pair of device electrodes, and the film comprising carbon (carbon film) 10 having the aforementioned configuration is disposed on the

substrate inside the gap 6 and on the conductive film 4 surrounding the gap 6.

This step forms the first gap 7 narrower than the second gap 6 inside the second gap 6. In addition, due to the activation step, the current flowing between the electrodes 2 and 3 (device current I_f) incurs remarkable changes, and the electron emission current I_e also increases. The conclusion of the activation step is appropriately implemented while the device current I_f is being measured. Incidentally, the pulse width, the pulse interval, the pulse wave height value, etc. are appropriately set.

The current flows between the electrodes 2 and 3, which shows that the film comprising carbon 10 having been formed in the activation step is electronically connected with the electrodes 2 and 3.

In addition, for the purpose of forming the region having orientation in the approximately parallel direction to the aforementioned substrate surface and the region not showing any particular orientation (disordered region), it is preferable to perform a step of removing gas while heating the device and the substrate 1 before implementing the activation step after the above-described forming step. In addition, removing gas while heating as mentioned above will preferably provide a pressure lower than the above-mentioned pressure at the time of forming step, and

moreover, the gas pressure introduced in the present activation step is more preferably lower than the above-mentioned pressure at the time of forming step.

5 5) The electron-emitting device obtained over the above-described step preferably undergoes a stabilization step. This step is a step of removing organic substance molecules, etc. adsorbed to the electron-emitting devices. This step is implemented by disposing the above-mentioned electron-emitting devices
10 inside the vacuum container and removing gasses inside the container.

 As the vacuum apparatus to be used in this step, the one not using oil is preferable so that the oil spilt out from the apparatus may not proliferate to
15 inside the vacuum container. In particular, they are a vacuum apparatus in combination of an adsorption pump and an ion pump, etc. This evacuation will preferably produce allocated pressure of organic components inside the vacuum container at not more than 1×10^{-8} Torr
20 being allocated pressure which will not cause the above-mentioned carbon and carbon compound to almost newly deposit, and moreover, especially preferably at not more than 1×10^{-10} Torr. In addition, when the vacuum container is evacuated inside, it is preferable
25 that the whole vacuum container is heated so that the organic substance molecules absorbed by the interior walls of the vacuum container and the electron-emitting

devices can be easily removed.

At this time, the heating condition falls within the range of 80 to 300°C and preferably is 150°C or higher with which the processing preferably continues as long as possible, but heating will not especially be limited to this condition, but heating will be implemented under conditions appropriately selected according to respective conditions such as sizes and shape of the vacuum container, configuration of the electron-emitting device, etc. It is also necessary to lower the pressure inside the vacuum container (the total pressure) to the utmost, and the preferable pressure is 1×10^{-7} Torr or less, and moreover, 1×10^{-8} Torr or less is especially preferable.

The above-described atmosphere at the time of driving after having undergone the stabilization processing preferably maintains the atmosphere at the time of conclusion of the above described stabilization processing, but without limitation thereto, if organic substances are sufficiently removed, sufficiently stable feature can be maintained even if the state of vacuum might be more or less worse.

Undergoing such a step, any new deposit of carbon or carbon compound onto the elements can be controlled.

In addition, H_2O and O_2 , etc. which absorbed by the vacuum container and the substrate, etc. can be removed, and as a result, the device current I_f and the

emission current I_e are stabilized.

Basic features of the electron-emitting device to which the present invention having been obtained undergoing the above-described step is applicable are described with reference to FIG. 3 and FIG. 7.

FIG. 3 is a schematic diagram drawing showing an example of the vacuum processing device, and this vacuum processing device is also equipped with functions to work as a measurement evaluation system.

In FIG. 3, a vacuum container is numbered as 35, and the ventilation pump is numbered as 36. Inside the vacuum container 35, the electron-emitting device which has completed steps up to the aforementioned stabilization step is disposed. That is, a substrate configuring the electron-emitting device is numbered 1, electrodes are numbered 2 and 3, a conductive film is numbered 4, an electron-emitting region being the region adjacent the aforementioned gap 7 is numbered 5.

A power source to apply the device voltage V_f to the electron-emitting device is numbered 31, an ammeter to measure the device current I_f flowing through the conductive film 4 between the electrodes 2 and 3 is numbered as 30, and an anode electrode to capture the emission current I_e emitted from the electron emission portion 5 is numbered 34. A high voltage power source to apply a voltage to the anode electrode 34 is numbered 32, and an ammeter to measure the emission

current I_e due to electron emission by the device is numbered 33. As an example, the measurement can be implemented by involving the voltage of the anode electrode being set to fall within the range of 1 kV to 10 kV and the distance H between the anode electrode and the electron-emitting device being set to fall within the range of 2 mm to 8 mm. In addition, inside the vacuum container 35, equipment necessary to implement measurement under vacuum atmosphere such as a vacuum meter, etc. is provided so that measurement and evaluation under a desired vacuum atmosphere can be implemented. In the case where the one which the power source 31 can supply with sufficient power is used, this device can proceed with the above-described forming step as well. In addition, moreover, the entire vacuum processing device and device can be heated by a heater to be usable to the above-mentioned stabilization step.

FIG. 7 is a drawing having schematically shown the relationships between the emission current I_e of the electron-emitting device of the present invention and the device voltage V_f and between the device current I_f and the device voltage V_f which have been measured using the vacuum processing device shown in FIG. 3. In FIG. 7, the emission current I_e is remarkably small compared with the device current I_f , thus shown in arbitrary units. Incidentally, the vertical axis and

the horizontal axis are scaled linearly.

As being obvious from FIG. 7, the electron-emitting device of the present invention comprises three characteristic referred to the emission current
5 I_e .

That is,

(i) With the present device to which a device voltage not less than a certain voltage (V_{th} called threshold value voltage in FIG. 7) is applied, the emission
10 current I_e increases rapidly, and on the other hand, for a voltage not more than the threshold value voltage V_{th} , the emission current I_e is scarcely detected.

In other words, the device is a non-linear device having an obvious threshold value voltage V_{th} toward
15 the emission current I_e .

(ii) Since the emission current I_e depends on the device voltage V_f in monotonous increasing, the emission current I_e can be controlled with the device voltage V_f .

(iii) The quantity of emission electrons captured by the anode electrode 34 depends on time during which the device voltage V_f is applied. That is, the quantity of electrons captured by the anode electrode 34 can be
20 controlled by time during which the device voltage V_f is applied.
25

As being understandable from the description so far, the electron-emitting device of the present

invention will be able to control its electron emission feature easily in accordance with the input signal.

When this nature is utilized, applications to various purposes such as electron sources and image forming

5 apparatus, etc., which are configured to comprise a plurality of electron-emitting devices to be disposed, are possible.

FIG. 7 shows an example where the device current If increases in monotonous basis toward the device voltage Vf (hereinafter to be referred to as "MI feature").

In addition, the electron-emitting device of the present invention not only takes shape of the aforementioned planar type configuration as shown in FIGS. 1A to 1C, but also can take configuration of vertical type as described below.

FIG. 21 is a schematic diagram drawing showing one example of a vertical type surface conduction type electron-emitting device to which the electron-emitting device of the present invention can be applied.

In FIG. 21, for the same portions as those shown in FIGS. 1A to 1C, the same numbers are applied in correspondence with the numbers indicated in FIGS. 1A to 1C. A step forming portion is numbered as 21. The substrate 1, the device electrodes 2 and 3, the conductive film 4, the electron emission portion 5 can be configured by the materials similar to those in the

case of the aforementioned planar type electron-emitting device. The step forming portion 21 can be configured by insulating materials such as SiO_2 , etc. which have been formed by vacuum evaporation method, printing method, and sputtering method, etc. The film thickness of the step forming portion 21 corresponds with the electrode interval L of the aforementioned planar type surface conduction type electron-emitting device, and can fall within the range of several thousand Å to several ten μm (micro meter). This film thickness is set considering producing method of the step forming portion and the voltage to be applied to between the device electrodes, but the range from several hundred Å to several micro meter is preferable.

The conductive film 4 is laminated upon the electrodes 2 and 3 after the device electrodes 2 and 3 and the step forming portion 21 have been formed. The electron emission portion 5 is formed on the side wall surface of the step forming portion 21 in FIG. 21, but depends on producing conditions, and forming conditions, etc., and thus the shape and the positions will not be limited to this.

In the vertical type as well, similarly to the planar type, the film comprising carbon 10 has an orientation as shown in FIGS. 1C, 18A, 18B, 19A and 19B. The difference with the planar type is in only the point that the reference of its orientation is the

substrate 1 for the planar type, and is the step forming member 21 for the vertical type. The vertical type can be caused to occupy a smaller area for the device itself toward the substrate compared with the planar type, thus can be more highly densely arranged and formed. Also in the case of the vertical type, the electron emission characteristic is similar to the electron emission characteristic of the aforementioned planar type.

Utilizing the features of the above-described electron-emitting device of the present invention, it is possible to form an electron source in which a plurality of the above-described electron-emitting devices are disposed on the substrate. In addition, various kinds of arrangement for electron-emitting devices are adopted. As an example, one involves a ladder-shaped disposition wherein a number of electron-emitting devices disposed in parallel are respectively connected at both ends each other and a number of lines of electron-emitting devices are disposed (called a line direction), and to the direction perpendicular with this wiring (called column direction) the controlling electrode (also called a grid) disposed upper the electron-emitting devices controls and drives electrons from the electron-emitting device. Other than this, nominated is the one wherein a plurality of electron-emitting devices are disposed in the X

direction and the Y direction in a matrix shape, and one party of electrodes of a plurality of electron-emitting devices disposed in the same line are commonly connected to the wiring of the X direction, and the other party of electrodes of a plurality of electron-emitting devices disposed in the same column are commonly connected to the wiring of the Y direction. The one like this is so called matrix formation. Firstly, the simple matrix formation will be described.

10 The surface conduction type electron-emitting device of the present invention has the features (i) through (iii) as aforementioned. That is, the emission electrons from the surface conduction type electron-emitting device can be controlled with the wave height value and width of the pulse-shaped voltage applied between the device electrodes facing each other for a voltage not less than the threshold voltage. On the other hand, for a voltage not more than the threshold voltage, emission will scarcely take place. According to this feature, also in the case where a number of electron-emitting devices are disposed, appropriate application of pulse-shaped voltage to respective devices can control the quantity of electron emission by selecting the surface conduction type electron-emitting devices in accordance with the input signals.

Based on this principle, an electron source substrate obtainable by disposing a plurality of

electron-emitting devices to which the present invention is applicable is described as follows using FIG. 8. In FIG. 8, a substrate is numbered as 1, wiring in the X direction is numbered as 82, and wiring in the Y direction is numbered as 83. The surface conduction type electron-emitting device is numbered as 84, and wiring knot is numbered as 85.

X direction wiring 82 in m units consists of D_{x1} , D_{x2} , ..., D_{xm} , and can be configured by conductive metal formed by using vacuum evaporation method, printing method, and sputtering method, etc. and the like.

Materials for wiring, film thickness, and width are appropriately designed. Y direction wiring 83 consists of wiring of n units, namely D_{y1} , D_{y2} , ..., and D_{yn} , and is formed similarly to X direction wiring 82. Not-shown inter-layer insulation layer is provided between these m units of X direction wiring 82 and n units of Y direction wiring 83 to electrically separate the both parties.

The not-shown insulation layer is configured by SiO_2 formed by using vacuum evaporation method, printing method, and sputtering method, etc. and the like. For example, the layer is formed into a desired shape on the entire surface or on a portion of the substrate 1 having formed X direction wiring 82, and film thickness, material, and, producing method are appropriately set so that especially the layer can

tolerate the potential at the intersection between X direction wiring 82 and Y direction wiring 83. X direction wiring 82 and Y direction wiring 83 have been respectively pulled out as external terminals.

5 A pair of electrodes (not shown) configuring the surface conduction type electron-emitting device 84 are electrically connected with m units of X direction wiring 82, n units of Y direction wiring 83, and the wiring knot 85 made of metal, etc.

10 As for materials configuring wiring 82 and wiring 83, materials configuring the wiring knot 85 and materials configuring a pair of device electrodes, a part or the whole of the component elements thereof may be common or may be respectively different. These
15 materials are appropriately selected from for example materials of the aforementioned device electrode. In the case where materials configuring the device electrode and materials of wiring are the same, wiring connected with a device electrode can be called as a
20 device electrode.

 X direction wiring 82 is connected with the not shown scanning signal application means which applies the scanning signal to select lines of surface conduction type electron-emitting devices 84 arranged
25 in the X direction. On the other hand, Y direction wiring 83 is connected with not-shown modulated signal generating means to modulate each column of the surface

conduction type electron-emitting devices 84 arranged
in the Y direction in accordance with the input signals
The driving voltage which is applied to each electron-
emitting device is supplied as differential voltage
5 between the scanning signal and the modulated signal to
be applied to the element.

In the above-described configuration, simple
matrix wiring is used to enable respective devices to
be selected independently and to drive independently.

10 Next, electron source of ladder-shaped formation
is described using FIG. 11.

FIG. 11 is a schematic diagram drawing showing one
example of electron source of ladder-shaped formation.
In FIG. 11, an electron source substrate is numbered as
15 1 and the electron-emitting device is numbered as 111.
The common wiring D_{x1} through D_{x10} to connect the
electron-emitting devices 111 is numbered as 112. A
plurality of the electron-emitting devices 111 are
disposed in parallel in the X direction on the
20 substrate 1 (this is called an element line). A
plurality of these device lines are disposed to
configure an electron source. Application of driving
voltage to between common wiring for each device line
can cause each device line to be driven independently.
25 That is, to device lines from which electron beam is
desired to be emitted a voltage not less than the
electron emission threshold value is applied, and to

device lines from which electron beam is not emitted a voltage not more than the electron emission threshold value is applied. For the common wiring D_{x2} through D_{x9} between each device line the same wiring can be adopted
5 for D_{x2} and D_{x3} , for example.

The manufacturing method of the present invention can be applied to any of the electron source based on the above-described methods.

The image forming apparatus which has been
10 configured using an electron source in the above-mentioned simple matrix formation is described using FIGS. 6A, 6B and 9. FIG. 9 is a schematic diagram drawing showing one example of the display panel of an image forming apparatus, and FIGS. 6A and 6B are
15 schematic diagram drawings of fluorescent film used for the image forming apparatus in FIG. 9.

In FIG. 9, the electron source substrate in which a plurality of electron-emitting devices are disposed is numbered as 1, a rear plate on which the substrate 1
20 is fixed is numbered as 91, and the face plate in which fluorescent film 94 and metal back 95, etc. are formed inside the glass substrate 93 is numbered 96. A supporting frame is numbered as 92 and to the supporting frame 92 a rear plate 91 and face plate 96
25 undergo junction using flit glass with low melting point and the like.

The electron-emitting device of the present

invention is numbered as 84. The X direction wiring and the Y direction wiring connected with a pair of device electrodes configuring the electron-emitting device of the present invention are respectively
5 numbered as 82 and 83.

The enclosure (vacuum container) 98 is configured by a face plate 96, a supporting frame 92 and a rear plate 91 as described above. Since the rear plate 91 is provided mainly for the purpose of reinforcing
10 strength of the substrate 1, thus when the substrate 1 itself has sufficient strength, a rear plate 91 as a separate body can be regarded unnecessary. That is, the supporting frame 92 is directly sealed to the substrate 1 and the exterior enclosure 98 may be
15 configured with the face plate 96, the supporting frame 92 and the substrate 1. On the other hand, a not-shown supporting member called a spacer can be disposed between the face plate 96 and the rear plate 92 to configure the enclosure 98 with sufficient strength
20 against the atmosphere pressure.

FIGS. 6A and 6B are schematic diagram drawings showing a fluorescent film 94. The fluorescent film 94 can be configured by only fluorescent body in the monochrome case. In the case of color fluorescent
25 film, the film can be configured by black conductive members 61 called black stripe or black matrix, etc. due to the arrangement of fluorescent body and

fluorescent body 62. The purpose to provide a black stripe and a black matrix is to lessen color mixture, etc. to an unnoticeable level by blackening the portions adjacent portions outside each fluorescent

5 body 62 to which necessary three basic color fluorescent bodies are allocated in the case of color display, and to control decrease in contrast due to reflection of outer lights in the fluorescent film 94. For the black stripe material, other than the material

10 involving normally used graphite as a main component, materials which has conductivity, and less transparency and reflection of lights can be used. The method to apply fluorescent body to a glass substrate 93 is not limited to monochrome or color, and precipitation

15 method and print processes, etc. can be adopted. Metal back 95 is normally provided on the interior surface of the fluorescent film 94. The purpose to provide a metal back is to improve brightness by causing lights toward the interior surface from radiation of the

20 fluorescent body to mirror-reflect to direction of the face plate 96, and to cause to act as electrode to apply electron beam acceleration voltage, and to protect the fluorescent body against damage due to bombing of negative ions generated inside the

25 exterior enclosure and the like. The metal back can be formed by implementing smoothing processing on the surface of interior surface of the fluorescent film

(normally called "filming") after the fluorescent film is formed, and thereafter depositing Al using vacuum evaporation method, etc.

5 The face plate 96 may be provided with a transparent electrode (not shown) to the exterior party of the fluorescent film 94 to further improve conductivity of the fluorescent film 94.

10 When the aforementioned sealing is implemented, in the color case, each color fluorescent body is required to correspond with the electron-emitting device, and sufficient positioning is implemented.

15 One example of manufacturing method of an image forming apparatus shown in FIG. 9 is described below. Up to the step of activation of each electron-emitting device configuring the electron source, the methods having already been described are implemented. Thereafter, the stabilization step is implemented, and then the electron source, image forming members, vacuum container forming members, etc. are bonding each other
20 with flit glass, etc., thereby assembly step is implemented, and the interior gas is removed and the exhaust tube is heated by a burner, etc. and sealed out. After this, according to necessity, getter processing is implemented. Alternatively, after the
25 assembly step is implemented, the forming step, activation step, and stabilization step may be implemented.

FIG. 10 is a schematic diagram drawing showing outline of the device to be used in the step after especially the enclosure has been assembled. The enclosure 98 is connected to the vacuum chamber 103 via ventilation tube 102, and moreover, is connected with the evacuation apparatus 105 via the gate valve 104. To the vacuum chamber 103, a pressure measure 106 and quadrupole mass spectrograph 107, etc. are attached for the purpose of measuring the interior pressure as well as the pressure allocated to each component in the atmosphere. Since it is difficult to measure the interior pressure of the enclosure 98, etc. directly, the pressure inside the vacuum chamber 103, etc. are measured.

The aforementioned stabilization step and the sealing step are implemented, for example, by heating the enclosure 98 to maintain an appropriate temperature of 80 to 300°C, and implementing evacuation through the exhaust tube 102 by the evacuation apparatus 105 without using oil such as ion pump and absorption pump, etc. to sufficiently lessen organic substances from the atmosphere, and by confirming this with the pressure meter 106 and quadrupole mass spectrograph 107, and thereafter heating the exhaust tube with a burner to melt, and sealing out the device.

Preferably, for the purpose of maintaining the pressure after sealing of the enclosure 98, getter

processing is implemented. In the case where evaporation-type getter is used, just before or after the enclosure 98 is sealed, the getter disposed in the predetermined position (not shown) inside the enclosure 98 is heated by using resistance heating or high frequency heating, etc. and the evaporation film is formed.

FIG. 12 is a schematic diagram drawing showing one example of a panel configuration in an image forming apparatus comprising an electron source in the ladder-shaped formation. The grid electrode is numbered as 120, the cavity for electron to come through is numbered as 121, and the terminals outside the container consisting of D_{ox1} , D_{ox2} , ... D_{oxm} are numbered as 122. The terminals outside the container consisting of G_1 , G_2 , G_n which are connected with the grid electrode 120 are numbered as 123.

The big difference between the image forming apparatus shown here and the image forming apparatus in a simple matrix formation shown in FIG. 11 is whether or not the device comprises the grid electrode 120 between the electron source and the face plate.

The grid electrode 120 is the one to modulate the electron beam emitted from the surface conduction type electron-emitting device, and for the purpose of causing the electron beam to pass through the stripe-shaped electrodes disposed in perpendicular with the

device lines in a ladder-shaped formation, one circular opening 121 each corresponding to each device is provided. The shape and the disposing position of the grid will not be limited to the one shown in FIG. 12.

5 For example, as an opening, a number of passing-through openings can be provided in a meshed formation, and the grid can be provided surrounding or in the vicinity of the surface conduction type electron-emitting device.

10 The terminals outside the container 122 and the terminals outside the grid container 123 are electrically connected with the not-shown controlling circuit.

15 Accordingly, the producing method of the image forming apparatus using the electron source having a ladder-shaped wiring is almost similar to that in the case of the image forming apparatus in the aforementioned simple matrix formation.

[Example 1]

20 The electron-emitting device formed by the present example is configured as schematically shown in FIGS. 1A and 1B.

The manufacturing steps of the electron-emitting device produced in the present example are described using drawings as follows.

25 Step-a

Quartz has been used as the substrate 1, and after cleaning this with detergent, pure water, and organic

solvent, the photoresist RD-2000N (produced by Hitachi Chemical Co., Ltd.) has been applied with spinner (2500 rpm for 40 seconds), and pre-baking has been implemented at 80°C for 25 minutes.

5 Next, using a mask corresponding to the device electrode pattern, contact exposure has been implemented, and developing using developer has been implemented, and post-baking at 120°C for 20 minutes has been implemented and thus the resist mask has been
10 formed.

 Next, Ni has been film-formed with the vacuum evaporation method. The film-forming rate has been 0.3 mm/second with film thickness being 10 nm.

 Next, the above-described substrate has been
15 dipped in acetone to melt the resist mask, and then the element electrodes 2 and 3 of Ni have been formed by lift-off. The electrode interval H is 2 μ m, and the electrode length W is 500 μ m. (FIG. 2A)

Step-b

20 Next, Cr has been film-formed so as to have 50 nm thickness with the vacuum evaporation method after cleaning with acetone, isopropanol, butyl acetate the substrate in which electrodes have been formed and drying it. Next, the photoresist AZ1370 (produced by
25 Hoechst Corp.) has been applied with spinner (2500 rpm for 30 seconds), and pre-baking has been implemented at 90°C for 30 minutes.

Next, with exposure and development using the mask an opening corresponding to the shape of the conductive film has been formed, and post-baking has been implemented at 120°C for 30 minutes to form resist mask.

Next, the substrate has been dipped into etchant ((NH₄)Ce(NO₃)₆/HCl/H₂O = 17 g/5 cc/100 cc) for 30 seconds so that the mask opening undergoes Cr etching, and then the resist has been delaminated by acetone to form Cr mask.

Next, the organic Pd compound solution (ccp-4230 produced by Okuno Chemical Industries Co., Ltd.) has been applied with spinner (800 rpm for 30 seconds), and baking has been implemented at 300°C for 10 minutes to form a conductive film made from PdO.

Next, the substrate has been dipped into the above-described etchant again to remove Cr mask, and by lift-off, a conductive film 4 of the desired pattern has been formed. (FIG. 2B)

20 Step-c

Next, the above-described device has been mounted on the device schematically shown in FIG. 3, and the gas inside the vacuum chamber 35 has been evacuated with a not-shown evacuation apparatus, and when the pressure has reached not more than 1.3×10^{-3} Pa, the triangular pulses with wave height value being gradually increased as shown in FIG. 4B have been

applied to between the electrodes 2 and 3. The pulse width T1 has been set at 1 msec, and the pulse interval T2 has been set at 10 msec. When the wave height value has reached approximately 5.0 V, forming process has been completed and the second gap 6 has been formed. (FIG. 2C)

Step-d

Next, the gas inside the vacuum chamber 35 has been further evacuated with the evacuation apparatus, and after the pressure has reached not more than 1.3×10^{-5} Pas, tolunitrile has been introduced to get the pressure of 1.3×10^{-4} Pa. At first, the rectangular pulses which inverse polarities have been repeatedly applied to between the device electrodes with the wave height value as shown in FIG. 13B being gradually increased. Here, the pulse width T3 has been set at 1 msec., and the pulse interval T4 has been set at 10 msec., and the wave height value has been gradually increased from 10 V to 15 V over 35 minutes. Thereafter, the rectangular pulses as shown in FIG. 13A which inverse polarities with the constant wave height value have been repeatedly applied to between the device electrodes. The wave height value has been set at 15 V, the pulse width T3 has been set at 1 msec., and the pulse interval T4 has been set at 10 msec. The present step has formed the carbon film 10 as well as the first gap 7 as shown in FIG. 2D.

Step-e

Next, the device has been heated to reach 150°C and maintained thereat while the gas inside the vacuum chamber 35 has been evacuated with the evacuation apparatus, then the pressure has reached 1.3×10^{-6} Pa.

Next, after the device has been returned to the room temperature, a voltage of 8 kV has been applied to the anode electrode 34, and the rectangular pulses with the constant wave height value have been applied to between the device electrodes, and features thereof have been measured. Incidentally, the distance between the anode electrode and the device has been set at 4 mm.

The device of the present example has been driven for a constant time period, and it has been found out that the device currents I_f and I_e have scarcely been reduced. In addition, the phenomena to be regarded as discharge have never been observed during this driving, and a device extremely stable in terms of electron emission characteristic has been obtained. Moreover, before and after the step e, decrease of film thickness of the carbon film 10 has scarcely been observed, thus it has been shown that the device is also thermally stable.

In addition, using FIB-TEM method, a cross-sectional observation on the form of the electron-emitting device of the example 1 has been implemented.

Here, the observation has been implemented with digital recording in use of an imaging plate. At first, the observation has taken place with a low magnification, it has been found out that not only inside the gap 6 in FIGS. 1A to 1C, but also on the conductive film surrounding it the film comprising carbon (carbon film) 10 with thickness of not less than the level of 10 nm has been formed. Next, when the carbon film has been observed at a higher magnification, there have existed portions over a wide range where lattice fringes (lattice image) orientated in the approximately normal direction ($\pm 30^\circ$) against the surface of underlining substrate (the substrate 1 or the conductive film 4) have been observed as shown in FIGS. 19A and 19B. Moreover, when the interval of those lattice fringes (lattice image) have been measured, the range has been observed to be from 3.5 to 4.7 Å.

Moreover, when the observation image of the carbon film on the conductive film has undergone Fourier transform to obtain diffraction pattern, there have existed portions over a wide range where diffraction ring having maximum intensity in the approximately normal direction ($\pm 30^\circ$) against the surface of underlining substrate (or the conductive film) have been measured. In addition, the interval of the lattice fringes (lattice image) obtained from the distance between the positions with maximum intensity

of diffraction ring and the origin point of the diffraction pattern is measured to be in a range of 3.5 to 4.7 Å. In addition, the intensity of the diffraction ring with maximum intensity in a direction
5 has been divided by the intensity of the diffraction ring in the direction perpendicular with the above-mentioned direction to give a ratio which have been measured to be 2.5 or more.

[Example 2]

10 The present example is a manufacturing method of the electron source of the matrix wiring schematically shown in FIG. 14, and of the image forming apparatus (FIG. 9) using this electron source. FIG. 14 is a partial plan view showing as a schematic diagram the
15 configuration of the electron source of the matrix wiring formed by the present example, and the sectional configuration along a polygonal line 15 - 15 in FIG. 14 is shown in FIG. 15. With reference to FIGS. 16A to 16D and FIGS. 17E to 17G, the manufacturing step of the
20 electron source is described, and moreover the manufacturing step of the image forming apparatus is also described as follows.

Step-A

25 Silicon oxide film of 0.5 μm has been formed by sputtering method on a blue plate glass which has been cleaned, and the product is treated as the substrate 1, and Cr 5 nm and Au 600 nm have been film-formed thereon

by vacuum evaporation method in succession, thereafter, the photoresist AZ1370 (produced by Hoechst Corp.) has been used to form the underlining wiring 82 by photolithography technology. (FIG. 16A)

5 Step-B

Next, the inter-layer insulation layer 141 made of silicon oxide film with thickness of 1 μm is deposited by sputtering method. (FIG. 16B)

 Step-C

10 A photoresist pattern to form contract holes 142 in the inter-layer insulation layer is produced, and with this as a mask, the inter-layer insulation layer 141 has undergone etching by RIE (Reactive Ion Etching) method using CF_4 and H_2 . (FIG. 16C)

15 Step-D

A mask pattern of photoresist (RD-2000N-41: produced by Hitachi Chemical Co., Ltd.) having openings corresponding to the pattern of the device electrode has been formed, and Ti 5 nm and Ni 100 nm have been deposited thereon by vacuum evaporation method in succession, and next, the photoresist has been removed by an effective solvent, and the device electrodes 2 and 3 are formed by lift-off. The interval L between the device electrodes has been set at 3 μm . (FIG. 16D)

25 Step-E

The upper wiring 83 having lamination configuration of Ti 5 nm and Au 500 nm has been formed

by photolithography method using the photoresist similar to that in the step-A. (FIG. 17E)

Step-F

5 The conductive film 4 made of PdO has been formed by lift-off using the Cr mask similar to that in the step-b of the example 1. (FIG. 17F)

Step-G

10 The resist pattern covering other than the contact holes 142 has been formed, Ti 5 nm and Au 500 nm have been deposited in succession by vacuum evaporation, the resist pattern has been removed, unnecessary laminated film has been removed and the contact holes have been filled in, and the electron source substrate prior to forming has been produced. (FIG. 17G)

15 Using the above-described electron source substrate, the image forming apparatus having configuration shown in FIG. 9 has been produced.

20 The substrate 1 of the electron source has been fixed in the rear plate 91, and the face plate 96 has been disposed upper 5 mm of the substrate via the supporting frame 92, and flit glass has been applied on the bonding portions, and the temperature has been maintained at 400°C for 10 minutes in nitrogen atmosphere and bonding has been implemented to form the enclosure 98. The fluorescent film 94 and the metal back 95 have been formed on the interior surface of the face plate. The fluorescent film 94 shaped stripe

(FIG. 6A) has been adopted and formed by print processes. For the black conductive member, quality of the material comprising graphite as a main component has been used. The metal back has been formed by vacuum-evaporating Al after smoothing processing (filming) has been implemented on the interior surface of the fluorescent film.

At the time when the above-described assembly is implemented, it is necessary to proceed with corresponding to the fluorescent body and the electron-emitting device accurately, and the positioning has been conducted sufficiently. Incidentally, to inside the exterior enclosure, a getter (not shown) is also attached.

Step-H

The gas inside the above-mentioned enclosure has been evacuated with the not-shown evacuation apparatus (vacuum pump), and the triangular wave pulses have been applied similar to the step c of the example 1 to implement the forming step and the second gap 6 has been formed in each conductive film.

Step-I

In succession, tolunitrile has been introduced into the exterior enclosure similar to the step d of the example 1 to implement the activation step.

Step-J

Next, similarly to the step e of the example 1,

while the interior of the exterior enclosure has been undergoing evacuation, it has been heated and the stabilization step has been implemented, and as a result, the interior pressure has reached 1.3×10^{-6} Pa in approximately three hours.

Not-shown driving circuit has been attached to the exterior enclosure produced by the steps mentioned so far, and a high voltage of 10 kV has been applied to the metal back and the TV signals have been inputted to cause images to be displayed, then no phenomena regarded as discharge have not taken place, highly bright and highly minute images have been obtained on stable basis over a long time period.

[Example 3]

The electron-emitting device has been formed in steps similar to those in the example 1 except that the step-d of the example 1 has been changed to the step-D2 as shown below.

Step-D2

Next, the gas inside the vacuum chamber 35 has been evacuated by the evacuation apparatus 36, and after the pressure reach not more than 1.3×10^{-5} Pa, acrylonitrile has been introduced and the pressure has been set at 1.3×10^{-3} Pa. At first, the rectangular wave pulses which invert polarity while gradually increasing the wave height value as shown in FIG. 13B have been repeatedly applied to between the device

electrodes. Here, the pulse width T3 has been set at 1 msec. and the pulse interval T4 has been set at 10 msec., and the wave height value has been gradually increased from 10 V to 15 V over 35 minutes. At that
5 time, when the pulse voltage has not been applied to between the device electrodes, an electron beam has been radiated as pulses to the devices from the electron gun (not shown). Thereafter, the rectangular wave pulses which invert polarity at a constant wave
10 height value as shown in FIG. 13A have been repeatedly applied to between the device electrodes. The wave height value has been set at 15 V, and the pulse width T3 has been set at 1 msec. and the pulse interval T4 has been set at 10 msec. At that time, when the pulse
15 voltage has not been applied to between the device electrodes, an electron beam has been radiated as pulses to the devices from the electron gun (not shown). In the present example, the activation step has been implemented while the electron beams are
20 radiated to the carbon film.

The device of the present example has shown stable electron emission characteristic for a longer time period compared with the device of the example 1. Moreover, the film comprising carbon has been evaluated
25 using evaluation method similar to that in the example 1, then lattice fringes (lattice image) orientated in the approximately normal direction against the surface

of the substrate have been obviously observed over a wide range.

[Example 4]

5 The electron-emitting device having formed by the present invention is configured as schematically shown in FIGS. 1A and 1B.

The producing steps of the electron-emitting device having been produced in the present invention are described using drawings as follows.

10 Step-a

Quartz has been used as the substrate 1, and after cleaning this with detergent, pure water, and organic solvent, the photoresist RD-2000N (produced by Hitachi Chemical Co., Ltd.) has been applied with spinner (2500 rpm for 40 seconds), and pre-baking has been implemented at 80°C for 25 minutes.

Next, using a mask corresponding to the element electrodes 2 and 3 pattern, contact exposure has been implemented, and developing using developer has been implemented, and post-baking at 120°C for 20 minutes has been implemented and thus the resist mask has been formed.

Next, Ni has been film-formed with the vacuum evaporation method. The film-forming rate has been 0.3 mm/second with film thickness being 10 nm.

Next, the above-described substrate has been dipped in acetone to melt the resist mask, and then the

device electrodes 2 and 3 of Ni have been formed by lift-off. The electrode interval L is 2 μm , and the electrode length W is 500 μm . (FIG. 2A)

Step-b

5 Next, Cr has been film-formed so as to have 50 nm thickness with the vacuum evaporation method after cleaning with acetone, isopropanol, and butyl acetate the substrate in which electrodes have been formed and drying it. Next, the photoresist AZ1370 (produced by
10 Hoechst Corp.) has been applied with spinner (2500 rpm for 30 seconds), and pre-baking has been implemented at 90°C for 30 minutes.

Next, with exposure and development using the mask an opening corresponding to the shape of the conductive
15 film 4 has been formed, and post-baking has been implemented at 120°C for 30 minutes to form resist mask.

Next, the substrate has been dipped into etchant ($(\text{NH}_4)\text{Ce}(\text{NO}_3)_6/\text{HCl}/\text{H}_2\text{O} = 17 \text{ g}/5 \text{ cc}/100 \text{ cc}$) for 30 seconds
20 so that the mask opening undergoes Cr etching, and next the resist has been delaminated by acetone to form Cr mask.

Next, the organic Pd compound solution (ccp-4230 produced by Okuno Chemical Industries Co., Ltd.) has
25 been applied with spinner (800 rpm for 30 seconds), and baking has been implemented at 300°C for 10 minutes to form a conductive film made from small particles of

PdO.

Next, the substrate has been dipped into the above-described etchant again to remove Cr mask, and by lift-off, a conductive film 4 of the desired pattern
5 has been formed. (FIG. 2B)

Step-c (Forming step)

Next, the above-described device has been mounted on the apparatus schematically shown in FIG. 3, and the gas inside the vacuum chamber 35 has been evacuated
10 with the evacuation apparatus 36, and when the pressure has reached not more than 1.3×10^{-3} Pa, the triangular pulses with wave height value being gradually increased as shown in FIG. 4B have been applied to between the electrodes 2 and 3. The pulse width T1 has been set at
15 1 msec, and the pulse interval T2 has been set at 10 msec. When the wave height value has reached approximately 5.0 V, forming step has been completed and the second gap 6 has been formed. (FIG. 2C)

Step-d (Activation step)

20 Next, while the gas inside the vacuum chamber 35 has been being evacuated with the evacuation apparatus 36, the vacuum chamber 35 and the elements having finished undergoing the forming step have undergone baking at 150°C for two hours. And, when the
25 temperature has dropped to the room temperature, the pressure inside the vacuum chamber 35 has reached not more than 1.3×10^{-6} Pa.

Thereafter, tolunitrile has been introduced to inside the vacuum chamber 35 until the pressure has reached 1.3×10^{-6} Pa, which has been maintained for one hour until the pressure has been stabilized, and thereafter, the rectangular pulses which invert polarity have been applied to between the device electrodes 2 and 3 with the wave height value as shown in FIG. 13B being gradually increased. Here, the pulse width T3 has been set at 1 msec. and the pulse interval T4 has been set at 10 msec., and the wave height value has been gradually increased from 10 V to 15 V over 35 minutes. Thereafter, the rectangular wave pulses which invert polarity at a constant wave height value as shown in FIG. 13A have been repeatedly applied to between the element electrodes 2 and 3. The wave height value has been set at 15 V, and the pulse width T3 has been set at 1 msec. and the pulse interval T4 has been set at 10 msec. The present step has formed the carbon film 10 on the substrate 1 inside the second gap 6 formed in the above-described forming step as well as on the conductive film 4 in the vicinity of the second gap 6 (FIG. 2D). In addition, at the same time the first gap 7 has been formed.

Step-e

Next, the device has been heated to reach 150°C and maintained thereat while inside the vacuum chamber 35 has been evacuated, then the pressure inside the

vacuum chamber 35 has reached 1.3×10^{-6} Pa.

Next, after the device has been returned to the room temperature, a voltage of 8 kV has been applied to the anode electrode 34, and the rectangular pulses with
5 the constant wave height value have been applied to between the electrodes 2 and 3, and features thereof have been measured. Incidentally, the distance between the anode electrode and the device has been set at 4 mm.

10 The device of the present example has been driven for a constant time period, and it has been found out that the device currents I_f and I_e have scarcely been reduced. In addition, the phenomena to be regarded as discharge have never been observed during this driving,
15 and a device extremely stable in terms of electron emission characteristic has been obtained. Moreover, before and after the step e, decrease of film thickness of the film comprising carbon (carbon film) 10 has scarcely been observed, thus it has been shown that the
20 device is also thermally stable.

Next, using FIB-TEM method, a cross-sectional observation on the form in the step where the activation step of the present example has been finished has been implemented. Here, the observation
25 has been implemented with digital recording in use of an imaging plate. At first, the observation has taken place with a low magnification, it has been found out

that there exist portions where not only inside the gap
6 in FIGS. 1A to 1C but also on the conductive film 4
surrounding it the film comprising carbon 10 with
thickness of not less than the level of 10 nm has been
5 formed. Moreover, it has been confirmed that the
carbon films 10 are facing each other having the first
gap 7, width of which is narrower than the second gap
6, inside the second gap 6 between them. Next, the
deposits have been observed with higher magnification,
10 and the observation results as follows have been
obtained.

First, within the range of 100 nm from the end of
the film comprising carbon (carbon film) 10 facing the
first gap 7 toward the electrodes 2 and 3, there have
15 existed portions over a wide range in the carbon film
10 where lattice fringes (lattice image) orientated in
the approximately parallel direction (not less than -
45° and not more than +45° against the substrate
surface) to the surface of the substrate have been
20 observed (FIGS. 18A and 18B). Moreover, when the
interval of those lattice fringes (lattice image) have
been measured, the range has been observed to be from
3.5 to 4.3 Å. In addition, when the observation image
of the carbon film 10 in that region has undergone
25 Fourier transform to obtain diffraction pattern, there
have existed portions where diffraction ring having
maximum intensity in the vicinity of the parallel

direction (not less than -45° and not more than $+45^\circ$ against the substrate surface) to the surface of the substrate have been measured. In addition, the interval of the lattice fringes (lattice image) obtained from the distance between the positions with maximum intensity of diffraction ring and the origin point of the diffraction pattern has been within the range of 3.5 to 4.3 Å.

In addition, the intensity of the diffraction ring with maximum intensity in a direction has been divided by the intensity of the diffraction ring in the direction perpendicular with the above-mentioned direction to give a ratio which have been measured to be 2.5 or more.

In addition, in such place of the carbon film 10 that is apart from the aforementioned range to get closer to the electrodes 2 and 3, there have existed portions over a wide range where lattice fringes (lattice image) orientated in the approximately normal direction (not less than -30° and not more than $+30^\circ$ against the substrate surface) to the surface of the substrate have been observed as shown (FIGS. 19A and 19B). Moreover, when the interval of those lattice fringes (lattice image) have been measured, that interval has ranged from 3.7 to 4.7 Å. In addition, when the observation image of the carbon film 10 in that region has undergone Fourier transform to obtain

diffraction pattern, there have existed portions where
diffraction ring having maximum intensity in the
vicinity of the normal direction (not less than -30°
and not more than $+30^\circ$ against the substrate surface)
5 against the surface of the substrate have been
measured. Moreover, the interval of the lattice
fringes (lattice image) obtained from the distance
between the positions with maximum intensity of
diffraction ring and the origin point of the
10 diffraction pattern has been within the range of 3.7 to
4.7 Å. In addition, the intensity of the diffraction
ring with maximum intensity in a direction has been
divided by the intensity of the diffraction ring in the
direction perpendicular with the above-mentioned
15 direction to give a ratio which have been 2.5 or more.

Careful observation has been implemented on
borderline the portions where the lattice fringes
(lattice image) orientated in the vicinity of the
parallel direction (more than -45° and less than $+45^\circ$)
20 to the above-described substrate surface are observed
and the portions where the lattice fringes (lattice
image) orientated in the vicinity of the normal
direction (more than -30° and less than $+30^\circ$) to the
above-described substrate surface are observed, and as
25 shown in FIG. 20, in these portions, the lattice
fringes (lattice image) have not shown any particular
orientation.

[Example 5]

The present example is the producing method of the electron source of matrix wiring schematically shown in FIG. 14, and of the image forming apparatus (FIG. 9) using this electron source.

FIG. 14 is a partial plan view showing as a schematic diagram the configuration of the electron source of the matrix wiring formed by the present example, and the sectional configuration along a polygonal line 15 - 15 in the drawing is shown in FIG. 15. With reference to FIGS. 16A to 16D and FIGS. 17E to 17G, the manufacturing step of the electron source is described, and moreover the manufacturing step of the image forming apparatus is also described as follows.

Step-A

Silicon oxide film of 0.5 μm has been formed by a sputtering method on a blue plate glass which has been cleaned, and the product is treated as the substrate, and Cr 5 nm and Au 600 nm have been film-formed thereon by vacuum evaporation method in succession, thereafter, the photoresist AZ1370 (produced by Hoechst Corp.) has been used to form the underlining wiring 82 by photolithography technology. (FIG. 16A)

Step-B

Next, the inter-layer insulation layer 141 made of silicon oxide film with thickness of 1 μm is deposited by sputtering method. (FIG. 16B)

Step-C

A photoresist pattern to form contract holes 142 in the inter-layer insulation layer is produced, and with this as a mask, the inter-layer insulation layer 141 has undergone etching by RIE (Reactive Ion Etching) method using CF_4 and H_2 . (FIG. 16C)

Step-D

A mask pattern of photoresist (RD-2000N-41: produced by Hitachi Chemical Co.) having openings corresponding to the pattern of the element electrode has been formed, and Ti 5 nm and Ni 100 nm have been deposited thereon by vacuum evaporation in succession, and next, the photoresist has been removed by an organic solvent, and the device electrodes 2 and 3 are formed by lift-off. The interval between the device electrodes has been set at 3 μm . (FIG. 16D)

Step-E

The upper wiring 83 having lamination configuration of Ti 5 nm and Au 500 nm has been formed by photolithography method using the photoresist similar to that in the step-A. (FIG. 17E)

Step-F

The conductive film 4 made of PdO has been formed by lift-off using the Cr mask similar to that in the step-b of the example 1. (FIG. 17F)

Step-G

The resist pattern covering other than the contact

holes 142 has been formed, and Ti 5 nm and Au 500 nm have been deposited in succession by vacuum evaporation, and the resist pattern has been removed and unnecessary laminated film has been removed and the contact holes have been filled in, and the electron source substrate prior to forming has been produced. (FIG. 17G)

Using the above-described electron source prior to forming step, the image forming apparatus having configuration shown in FIG. 9 has been produced.

The above-described substrate 1 of the electron source prior to forming step has been fixed in the rear plate 91, and the face plate 96 has been disposed upper 5 mm of the substrate 1 via the supporting frame 92, and flit glass has been applied on the bonding portions, and the temperature has been maintained at 400°C for 10 minutes in nitrogen atmosphere and bonding has been implemented to form the enclosure. The fluorescent film 94 and the metal back 95 have been formed on the interior wall surface of the face plate. The fluorescent film 94 shaped stripe (FIG. 6A) has been adopted and formed by print processes. For the black conductive member, quality of materials comprising graphite as a main component has been used. The metal back has been formed by vacuum-evaporating Al after smoothing processing (filming) has been implemented on the interior surface of the fluorescent

film.

At the time when the above-described assembly is implemented, it is necessary to proceed with corresponding to the fluorescent body and the electron-emitting device accurately, and the positioning has been conducted sufficiently. Incidentally, to inside the enclosure, a getter (not shown) is also attached.

Step-H

The above-described enclosure has been connected with the evacuation apparatus via the not-shown exhaust tube, and the gas inside the enclosure has been evacuated to reach 1.3×10^{-5} Pa. And thereafter, through each wiring, the triangular wave pulses have been applied similarly to the step-c of the example 1 to implement the forming step and the first gap has been formed.

Step-I

In succession, the activation processing has been implemented under the same conditions as the step-d of the example 4, and the film containing carbon has been formed.

Step-J

Next, similarly to the step-e of the example 4, while the interior of the enclosure has been evacuated, it has been heated and the stabilization step has been implemented. And as a result, the interior pressure of the enclosure has reached 1.3×10^{-6} Pa in approximately

three hours.

Similar to in the example 4, the electron emission characteristic has been measured, revealing that all the devices have emitted electrons normally.

5 Not-shown driving circuit has been attached to the enclosure produced by the steps mentioned so far, and a high voltage of 10 kV has been applied to the metal back and the TV signals have been inputted to cause images to be displayed, then no phenomena regarded as
10 discharge have not taken place, highly bright and highly minute images have been obtained on stable basis over a long time period.

[Comparing example]

In the present comparing example, the electron-
15 emitting device has been produced with steps from the step-a through the step-c being similar to those in the example 4.

Step-d

Next, while the gas inside the vacuum chamber 35
20 has been being evacuated with the evacuation apparatus 36, the pressure has reached not more than 1×10^{-6} Pa. Thereafter, acetone has been introduced until the pressure has reached 1.3×10^{-2} Pa and after waiting until the pressure has been stabilized, the rectangular
25 pulses which inverse polarities have been applied to between the electrodes 2 and 3 with the wave height value as shown in FIG. 15 being gradually increased.

Here, the pulse width T3 has been set at 1 msec., and the pulse interval T4 has been set at 10 msec., and the wave height value has been gradually increased from 10 V to 15 V over 35 minutes. Thereafter, the rectangular pulses as shown in FIG. 13A which inverse polarities with the constant wave height value have been repeatedly applied to between the device electrodes. The wave height value has been set at 15 V, the pulse width T3 has been set at 1 msec., and the pulse interval T4 has been set at 10 msec.

Step-e

Next, the device has been heated to reach 150°C and maintained thereat while the gas inside the vacuum chamber 35 has been evacuated with the evacuation apparatus 36, then the pressure has reached 1.3×10^{-6} Pa.

Next, after the device has been returned to the room temperature, similar to in the example 1, a voltage of 8 kV has been applied to the anode electrode 34, and the rectangular pulses which inverse polarities with the constant wave height value have been applied to between the device electrodes, and features thereof have been measured. Incidentally, the distance between the anode electrode and the device has been set at 4 mm.

The device of the present comparing example has been driven for a constant time period, revealing that

the device currents I_f and emission current I_e have been gradually reduced. In addition, the phenomena to be regarded as discharge have been observed several time during this driving.

5 Next, similar to in the example 4, using FIB-TEM method, a cross-sectional observation on the form of the electron-emitting device of the present comparing example has been implemented. At first, the observation has taken place with a low magnification,
10 it has been found out that there exist portions where not only inside the gap but also on the conductive film surrounding it the film comprising carbon 10 with thickness of not less than the level of 10 nm has been formed. Next, when the deposits have been observed at
15 a higher magnification, the observation results as follows have been obtained.

 At first, in the region apart from the first gap 7 by 100 nm, lattice fringes (lattice image) have been observed at some portions, but no particular
20 orientations have been shown.

 Next locations beyond the region at 100 nm from the above-described first gap 7 have been observed, but no places where the lattice fringes (lattice image) are observed have not have not been able to be found out.

25 As described so far, in the electron-emitting device of the present invention, the film comprising carbon which has been deposited on the substrate inside

the gap having formed in the conductive film and on the conductive film is orientated in the approximately normal direction against the substrate surface and/or the conductive film surface.

5 Moreover, in the region closest to the electron emission portion, that is, in the location where two parties are facing each other via the first gap, the above-described lattice fringes (lattice image) of the film comprising carbon are orientated in the
10 approximate parallel direction to the substrate surface.

 Therefore, the majority of the surface of the film comprising carbon (carbon film) contacting the vacuum is thermally and chemically stable.

15 Moreover, in the region where the film comprising carbon connects the region closest to the first gap 7, which has been orientated in the approximate parallel direction to the substrate surface, with the region
20 apart from the first gap 7, which has been orientated in the approximately normal direction against the substrate surface, it is thought that no particular orientation to be held will enable the film comprising carbon not to save any necessary stress. As a result
25 thereof, the shape of the film comprising carbon is thought to be thermally stable.

 Consequently, various kinds of evaporation from the carbon film and compositional change in carbon film

due to the temperature increase at the time of driving
of the electron-emitting device, and heating at the
time of assembling the image forming apparatus are
suppressed and moreover the influence by the absorption
5 of impurities, etc. is reduced.

According to the advantages described so far, the
electron-emitting device having electron emission
characteristic which is highly efficient and stable
over a long time period has been obtained.

10 Moreover, in the image forming apparatus using an
electron source in which a number of the electron-
emitting devices of the present invention have been
arranged and formed over a large area, the electron-
emitting devices are extremely stable even if they are
15 highly densely disposed to obtain highly minute images,
and such an image forming apparatus that has a long
life even if a higher anode voltage has been applied,
and is highly reliable and can provide highly bright
and highly quality images has been completed.

WHAT IS CLAIMED IS:

1. An electron-emitting device comprising a substrate, first and second carbon films disposed so as to have a first gap between the first and second carbon
5 films on a surface of the substrate, and first and second electrodes electrically connected with the first and the second carbon films respectively, wherein

the carbon film has a region showing orientation, and the direction of the orientation is an
10 approximately parallel to the surface of the substrate surface.

2. An electron-emitting device comprising a substrate, first electrode and second electrodes
15 disposed on a surface of the substrate, and a carbon film electrically connected to the first and second electrodes, wherein

the carbon film has a first gap at a portion in itself, and has a region showing orientation, and the
20 direction of the orientation is the approximately parallel to the surface of the substrate surface.

3. The electron-emitting device according to claim 1 or 2, wherein the direction of the orientation
25 is not less than -45 degrees and not more than +45 degrees against the substrate surface.

4. The electron-emitting device according to claim 1 or 2, wherein the region showing orientation faces the first gap.

5 5. The electron-emitting device according to claim 4, wherein said region showing orientation is disposed in the region of the distance of not more than 100 nm from the end portion of said carbon film facing said first gap toward the direction of said electrodes.

10 6. The electron-emitting device according to claim 1 or 2, wherein said carbon film further has a region orientated in an approximately normal direction against said substrate surface.

15 7. The electron-emitting device according to claim 6, wherein said approximately normal direction is not less than -30 degrees and not more than +30 degrees from normal direction against said substrate surface.

20 8. The electron-emitting device according to claim 6, wherein the region orientated in said approximately normal direction is disposed between the region orientated in said approximate parallel
25 direction and said electrodes.

9. The electron-emitting device according to

claim 6, wherein said carbon film has a region without
any particular orientation, and said region without any
particular orientation is disposed between the region
orientated in said approximate parallel direction and
5 the region orientated in said approximately normal
direction.

10. The electron-emitting device according to
claim 1 or 2, wherein said carbon film and the first
10 and the second electrodes are connected via a
conductive film.

11. An electron source comprising a plurality of
electron-emitting devices which have been arranged and
15 formed on a substrate, wherein the electron-emitting
device is the electron-emitting device according to
claim 1 or 2.

12. An image forming apparatus comprising an
20 electron source and an image forming member forming
images with electrons to be emitted from the electron
source being radiated, wherein the electron source is
an electron source according to claim 11.

25 13. An electron-emitting device comprising:
a substrate;
first and second electrodes respectively having

been disposed on the substrate surface;

first and second conductive films having a second gap disposed between said electrodes and respectively connected with said and the second electrode;

5 first and second carbon films having a first gap within said second gap and disposed so as to be respectively connected with said first and the second conductive film; wherein

10 said first and the second carbon film respectively covers a part of said first and the second conductive film, and

the carbon film disposed on said conductive film has a region showing orientation, and a direction of the orientation is approximately normal direction
15 against said substrate surface.

14. An electron-emitting device comprising:

a substrate;

20 first and second electrodes respectively having been disposed on said substrate surface;

a conductive film connected with said both electrodes and having a second gap disposed in one portion of itself;

25 a carbon film connected with said conductive film and having a first gap in one portion of itself; wherein

said first gap is disposed inside said second gap,

and said carbon film has, on said conductive film, a region showing orientation, and a direction of the orientation is approximately normal direction against said substrate surface.

5

15. The electron-emitting device according to claim 13 or 14, wherein the direction is not less than -30 degrees and not more than +30 degrees against the substrate surface.

10

16. The electron-emitting device according to claim 13 or 14, wherein said carbon film further has a region orientated in the approximately parallel direction to said substrate surface.

15

17. The electron-emitting device according to claim 16, wherein said approximately parallel direction is not less than -45 degrees and not more than +45 degrees along said substrate surface.

20

18. The electron-emitting device according to claim 16, wherein the region orientated in said approximate parallel direction faces said first gap.

25

19. The electron-emitting device according to claim 16, wherein said region orientated in said approximate parallel direction is disposed in the

region of the distance of not more than 100 nm from the end portion of said carbon film facing said first gap toward the direction of said electrodes.

5 20. The electron-emitting device according to claim 16, wherein the region orientated in said approximately normal direction is disposed between the region orientated in said approximate horizontal direction and said electrodes.

10

 21. An electron-emitting device according to claim 16, wherein said carbon film has a region without any particular orientation, and said region without any particular orientation is disposed between the region orientated in said approximate horizontal direction and the region orientated in said approximately normal direction.

15

 22. An electron source comprising a plurality of electron-emitting devices which have been arranged and formed on a substrate, wherein the electron-emitting device is the electron-emitting device according to any of claim 13 or 14.

20

25 23. An image forming apparatus comprising an electron source and an image forming member forming images with electrons to be emitted from the electron

source being radiated, wherein the electron source is
an electron source according to claim 22.

ABSTRACT OF THE DISCLOSURE

An electron-emitting device includes a substrate,
first and second carbon films disposed so as to have a
first gap between the first and second carbon films on
5 a surface of the substrate, and first and second
electrodes electrically connected with the first and
the second carbon films respectively, wherein the
carbon film has a region showing orientation, and a
direction of the orientation is in an approximately
10 parallel direction along the substrate surface.
Thereby, it is possible to improve thermal and chemical
stability of a carbon film and stabilize good electron
emission characteristics over a long period.

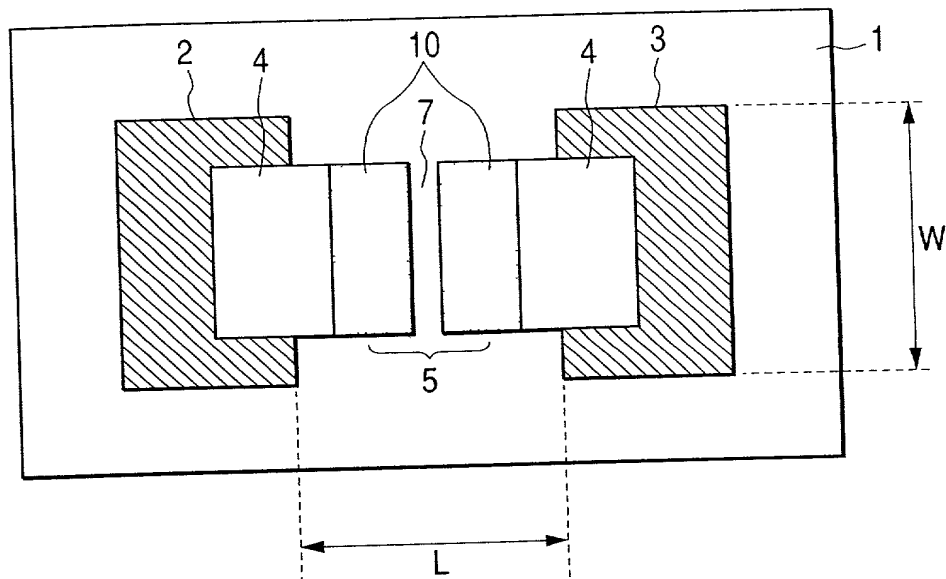
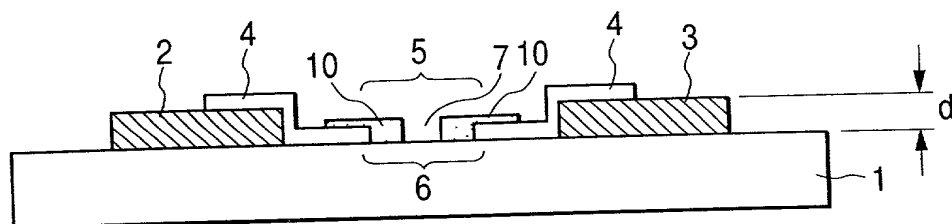
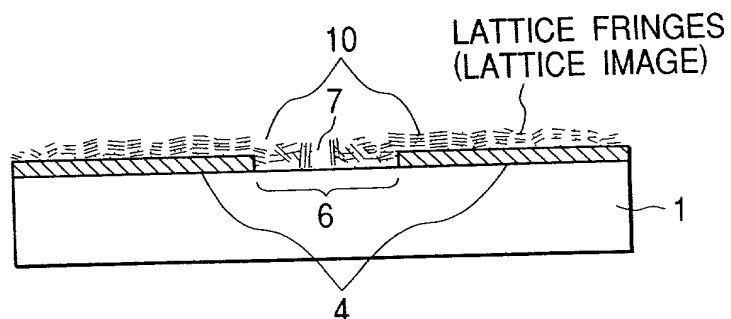
FIG. 1A**FIG. 1B****FIG. 1C**

FIG. 2A

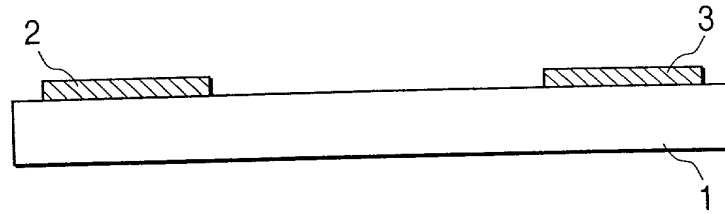


FIG. 2B

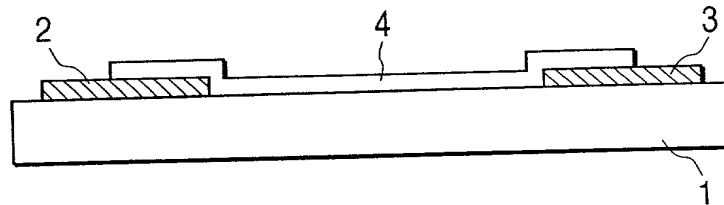


FIG. 2C

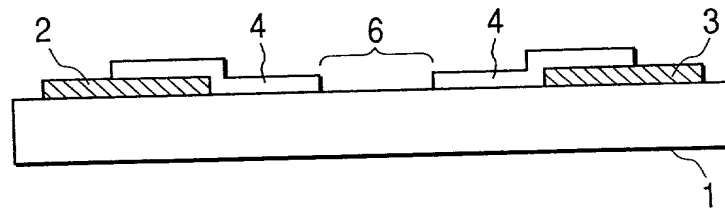


FIG. 2D

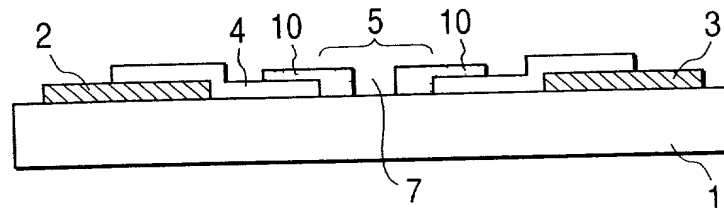


FIG. 3

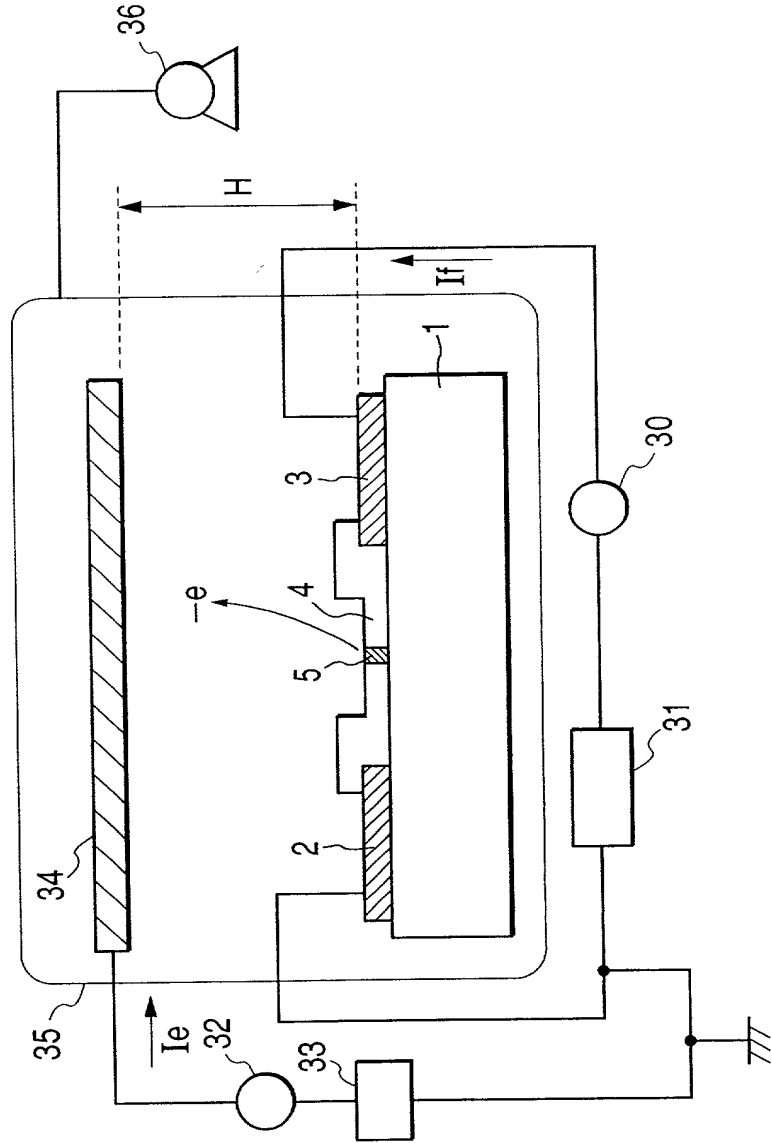


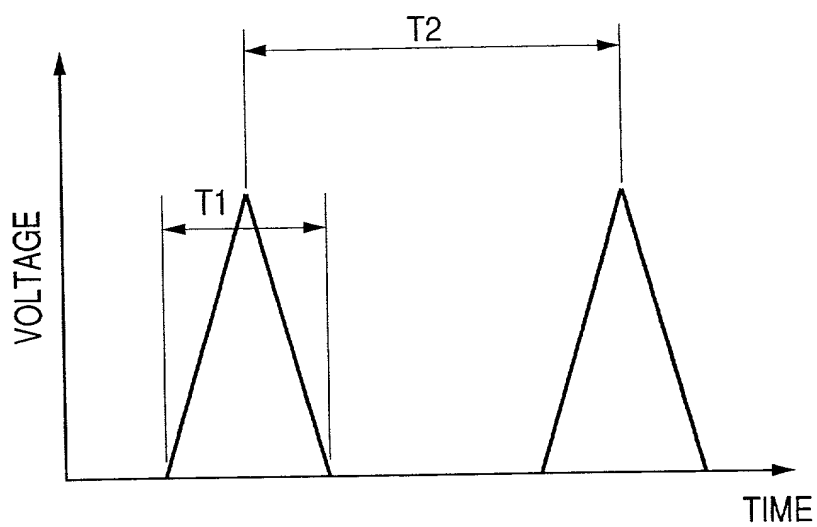
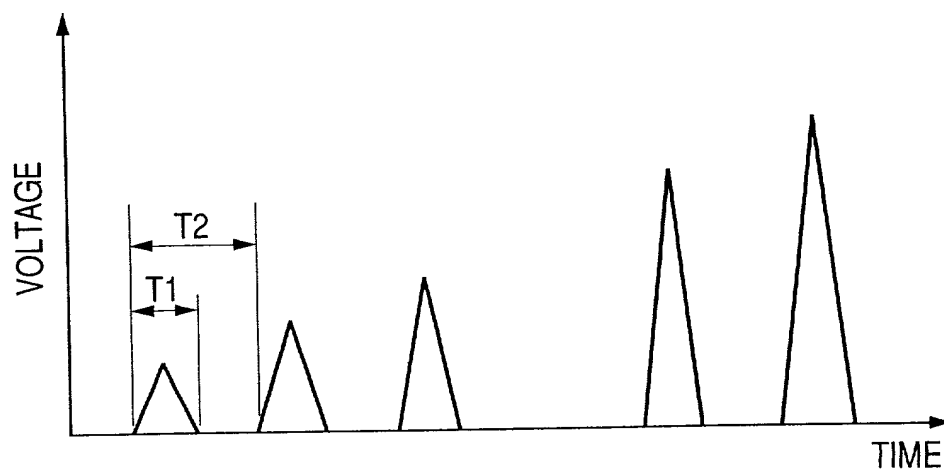
FIG. 4A**FIG. 4B**

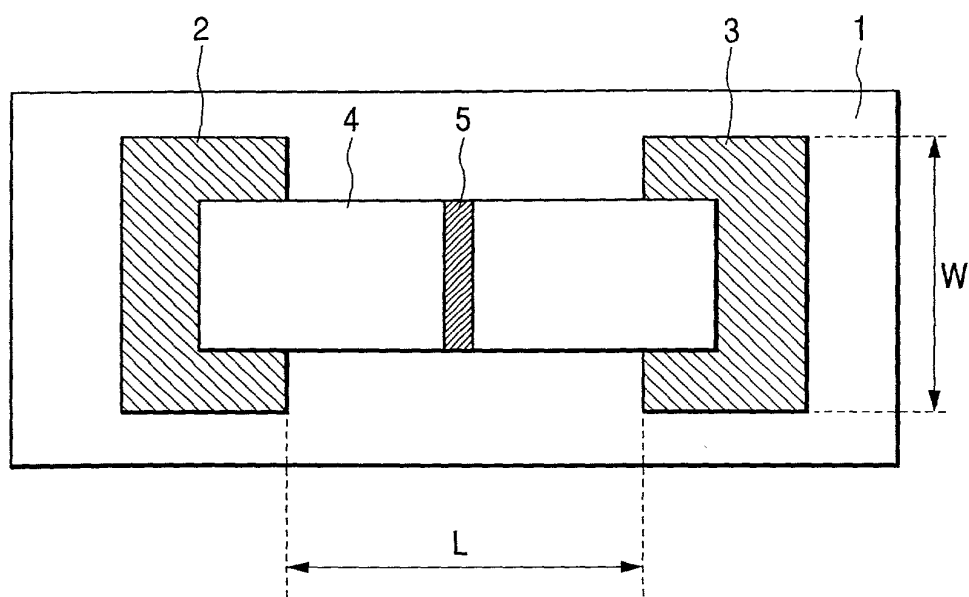
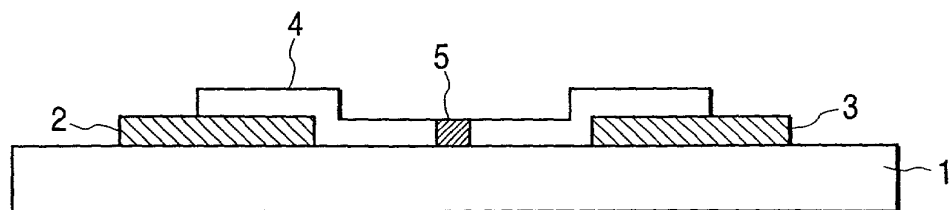
FIG. 5A*FIG. 5B*

FIG. 6A

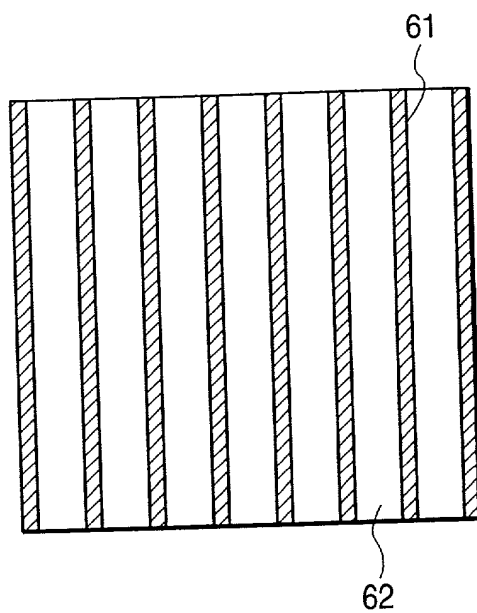


FIG. 6B

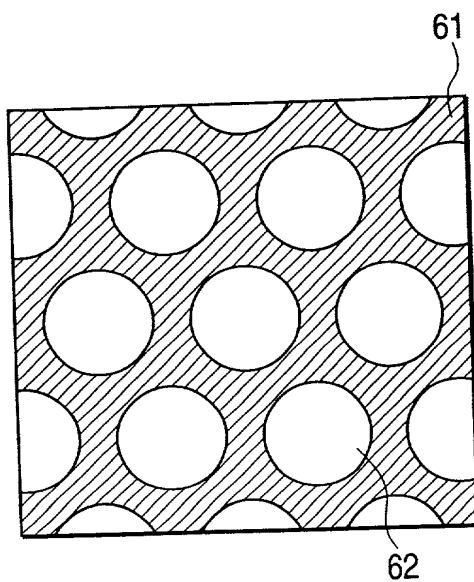


FIG. 7

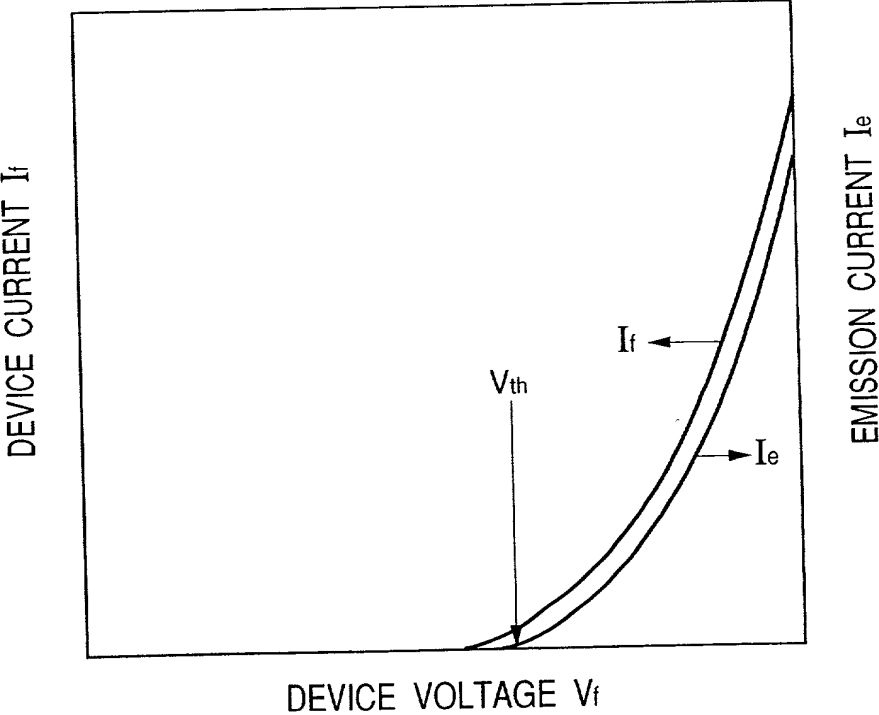


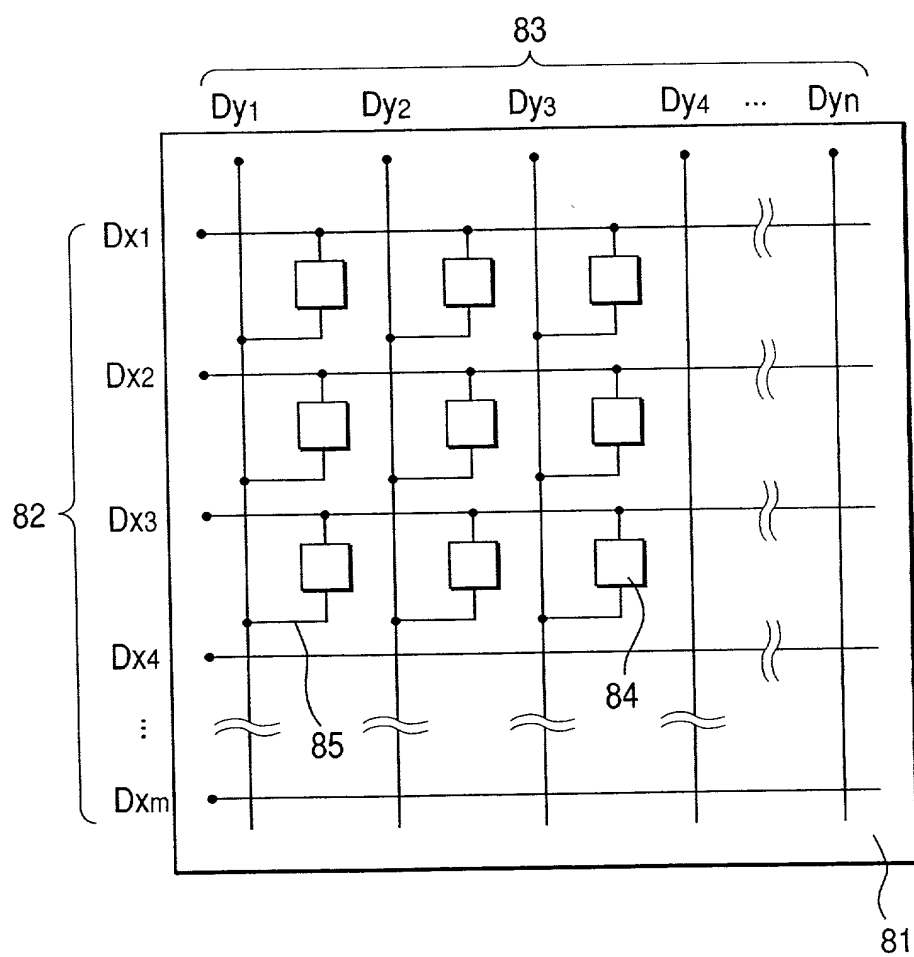
FIG. 8

FIG. 9

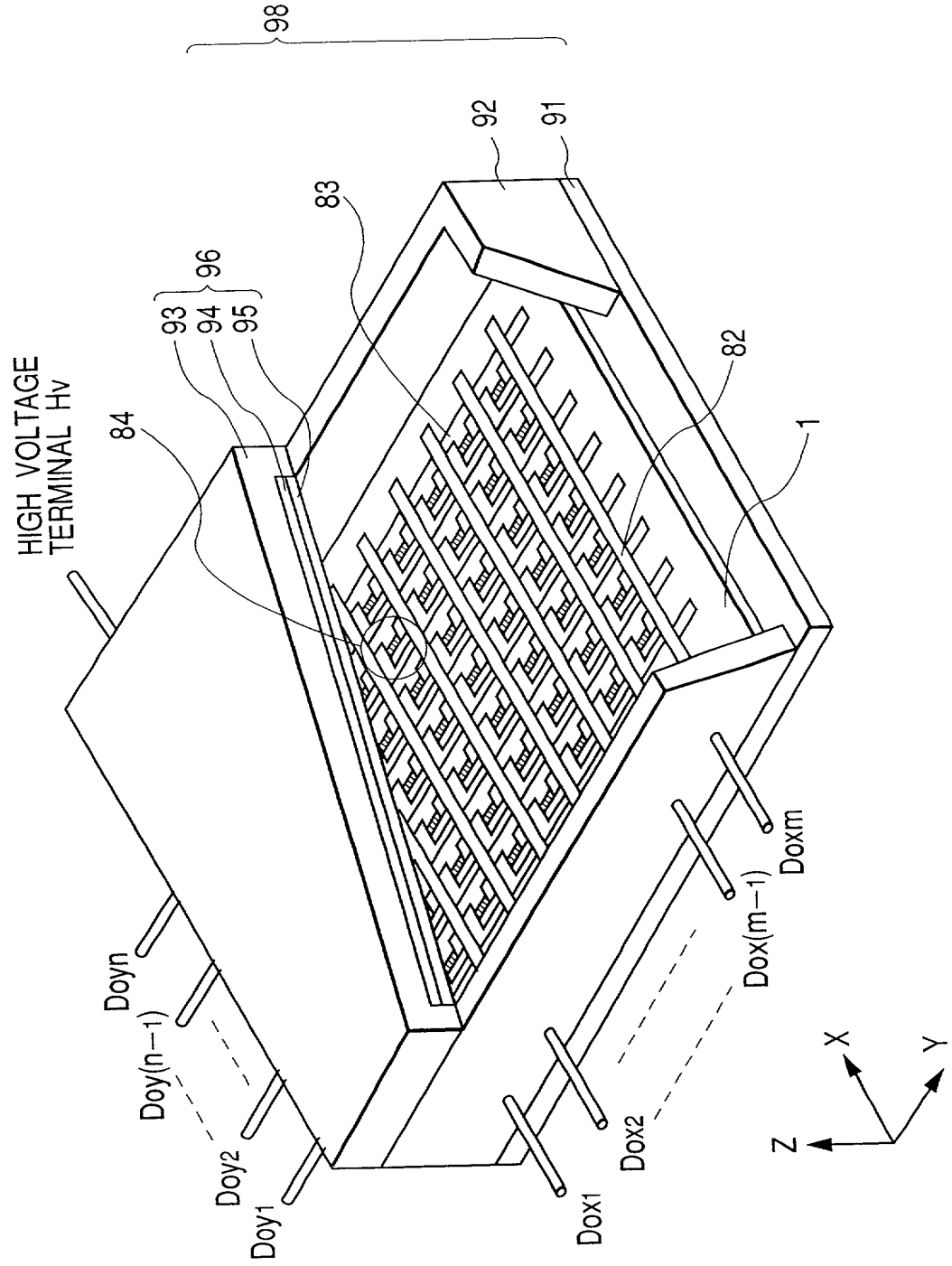


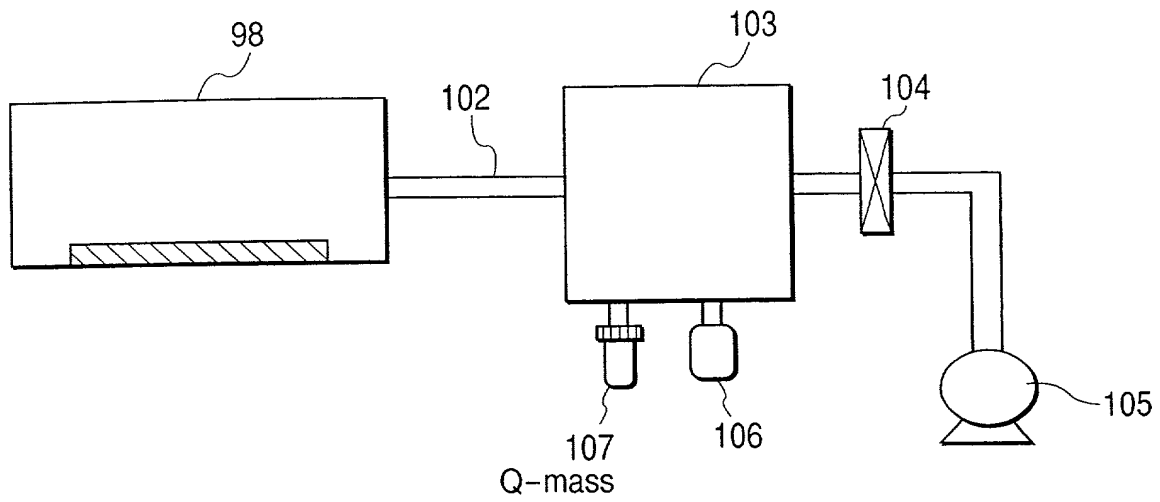
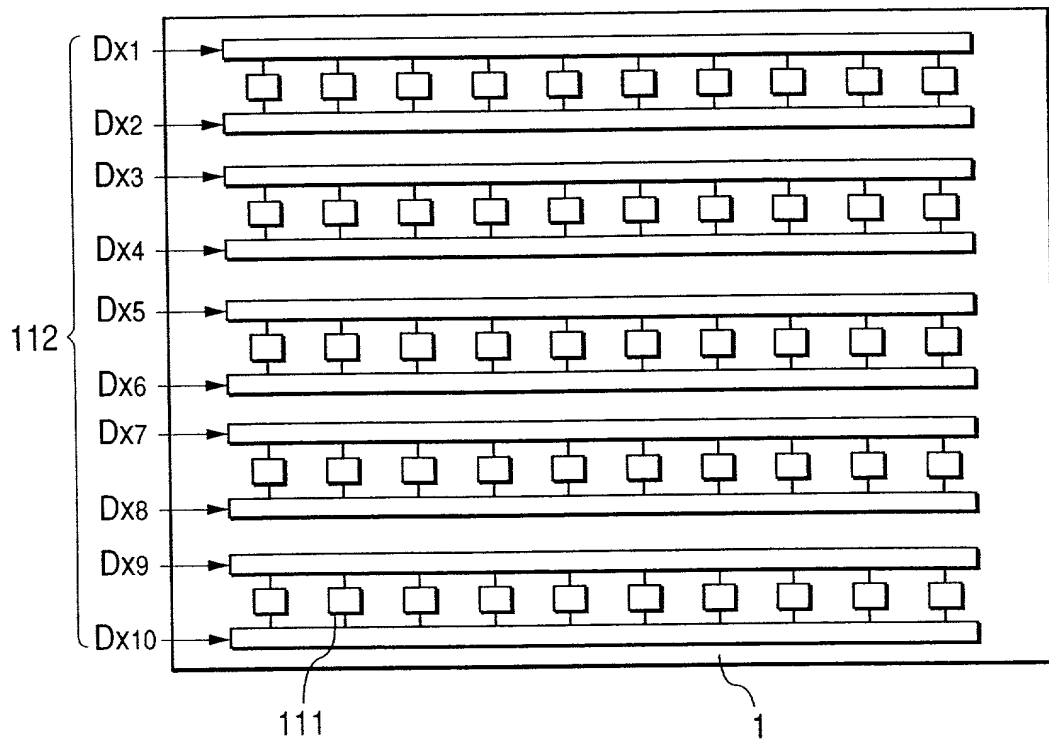
FIG. 10**FIG. 11**

FIG. 12

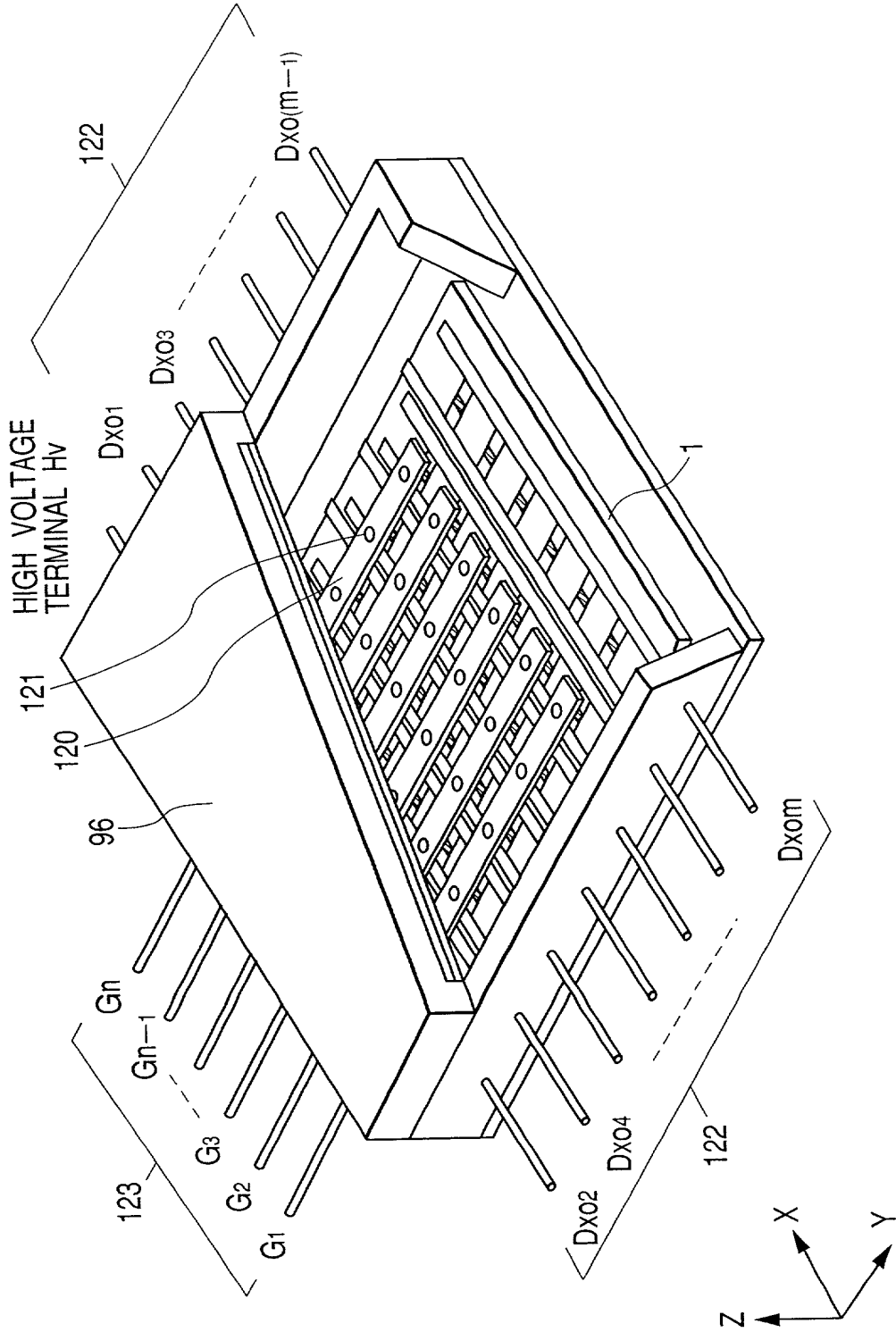


FIG. 13A

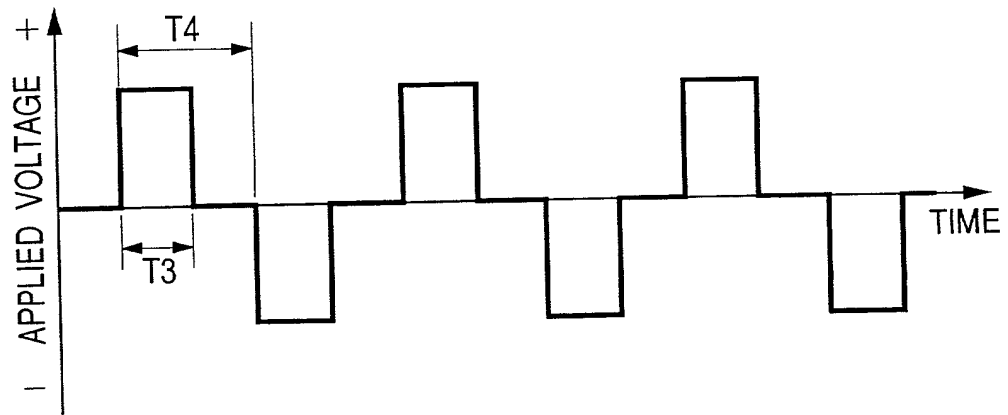


FIG. 13B

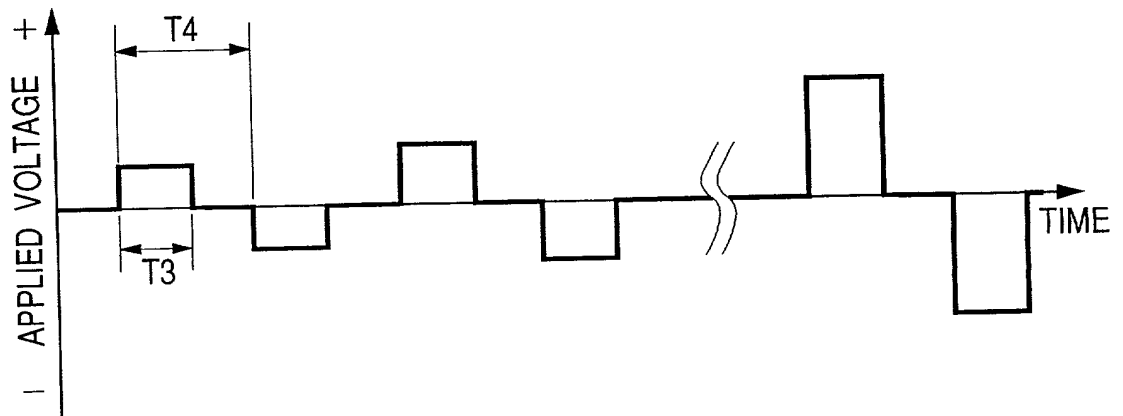


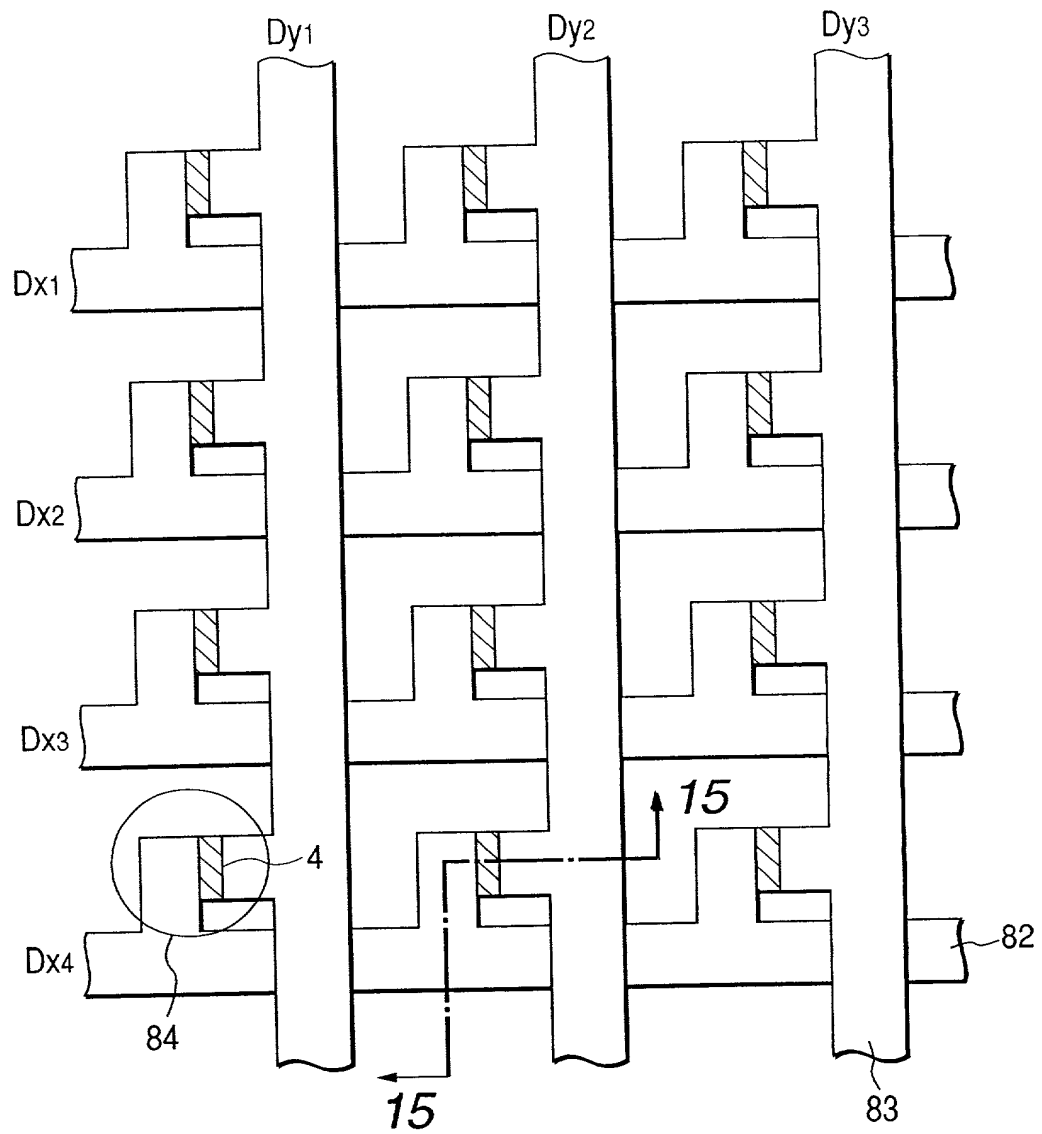
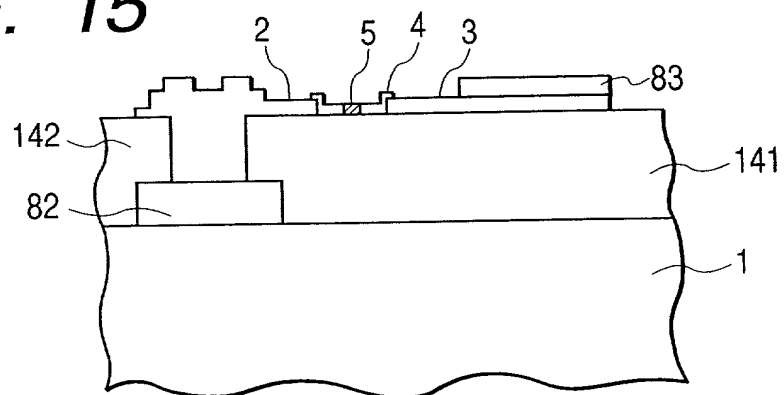
FIG. 14**FIG. 15**

FIG. 16A

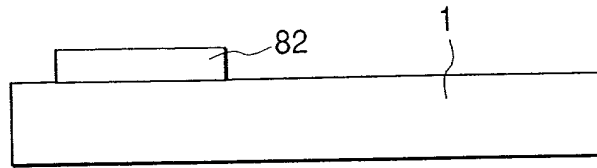


FIG. 16B

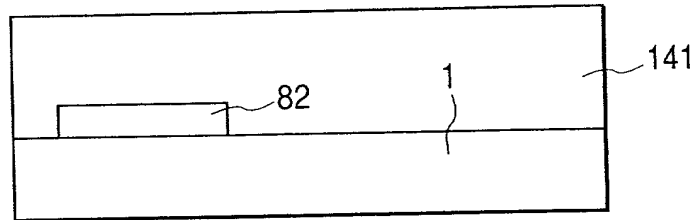


FIG. 16C

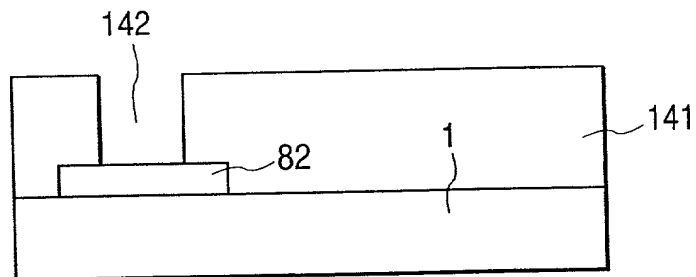


FIG. 16D

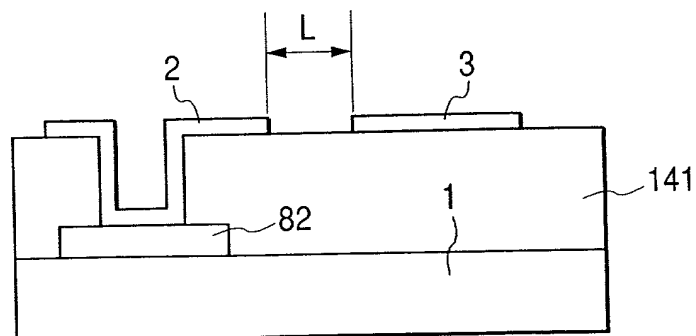


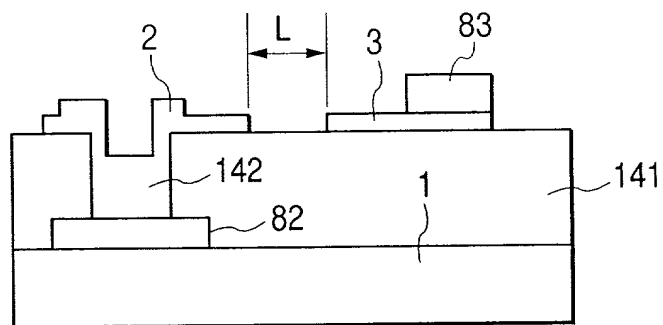
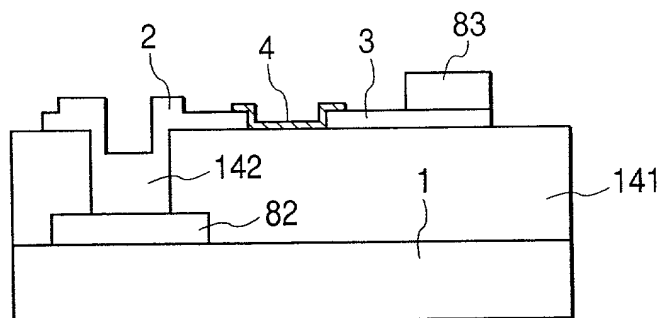
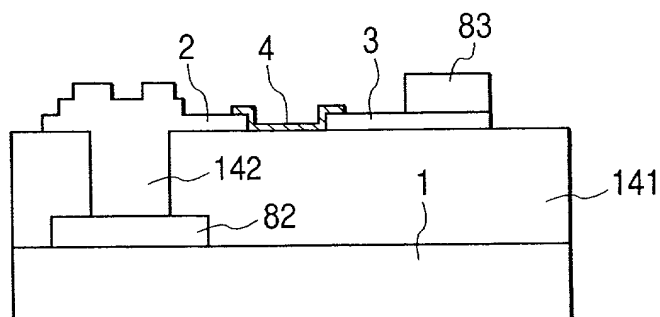
FIG. 17E**FIG. 17F****FIG. 17G**

FIG. 18A

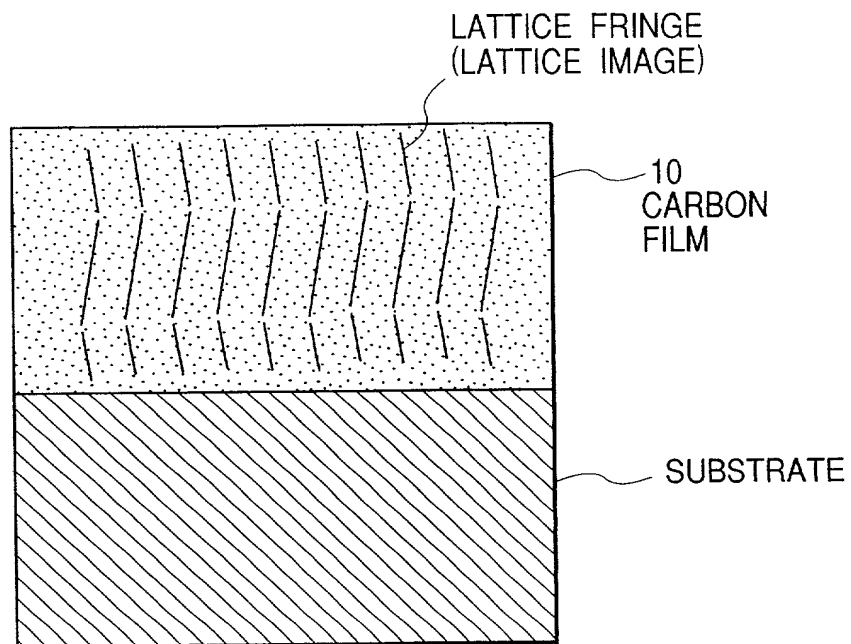


FIG. 18B

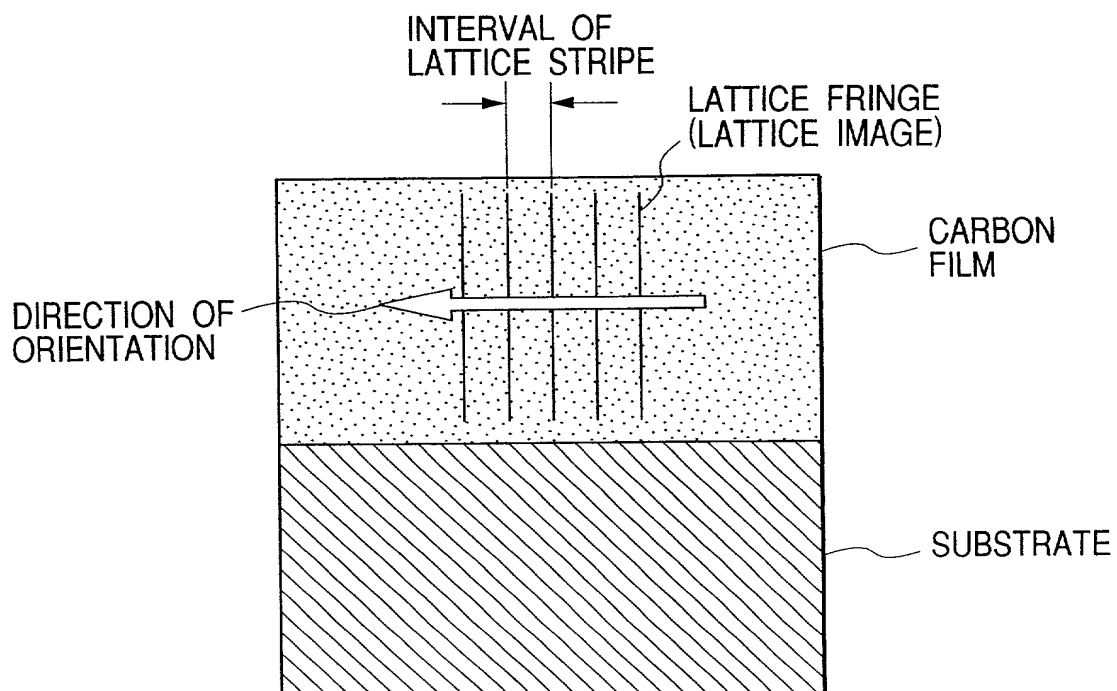


FIG. 19A

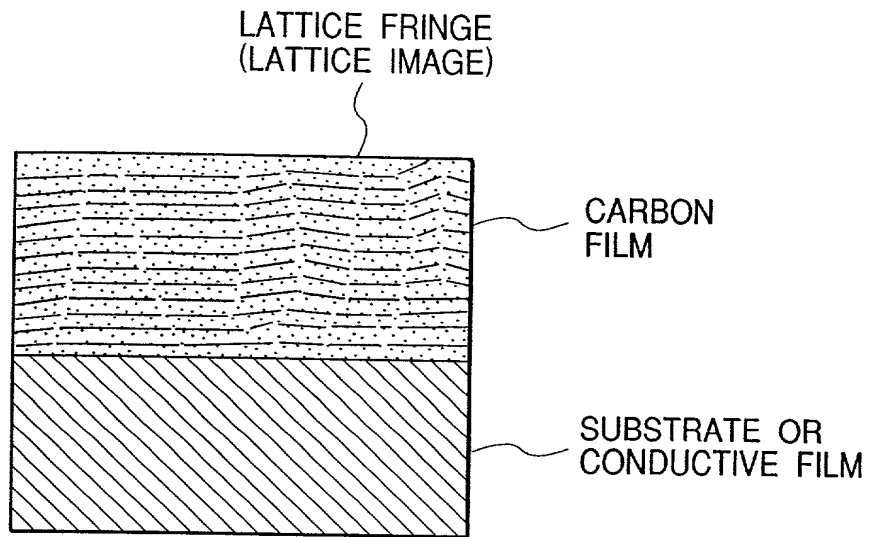


FIG. 19B

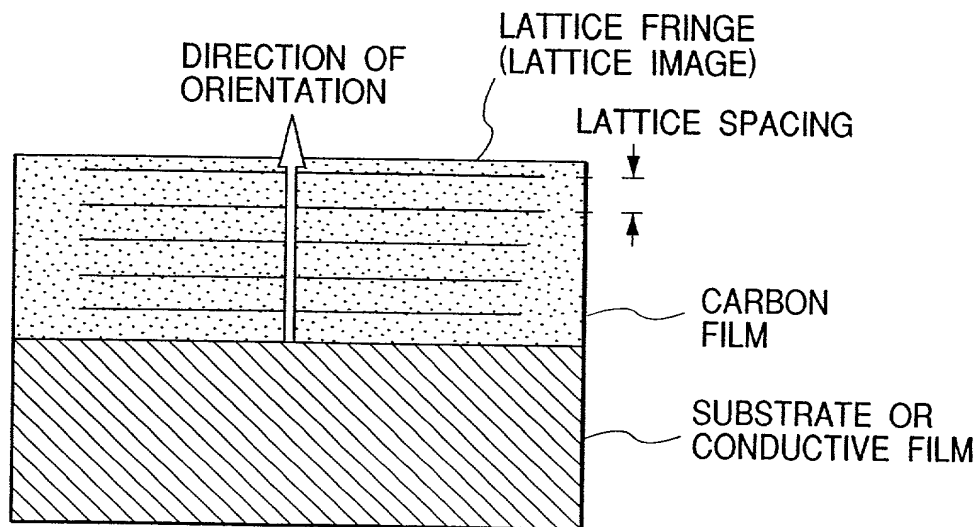


FIG. 20

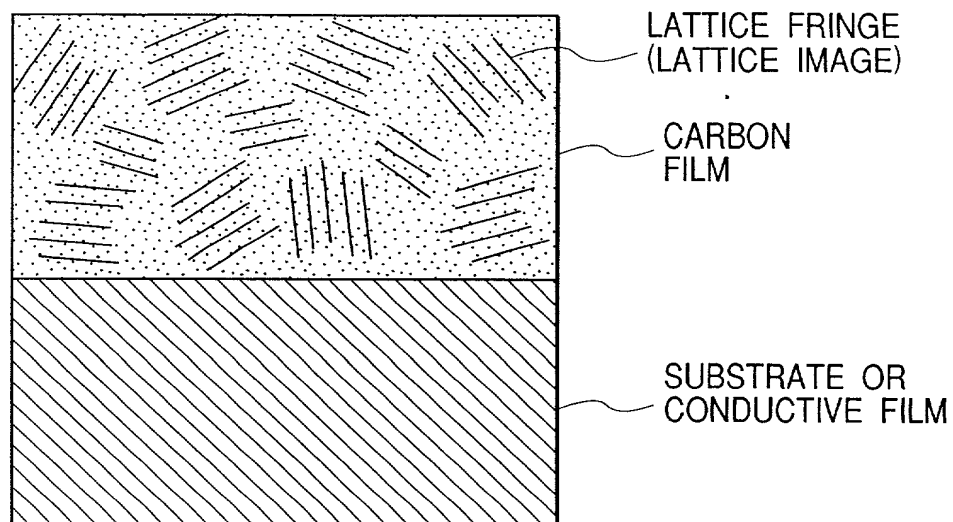


FIG. 21

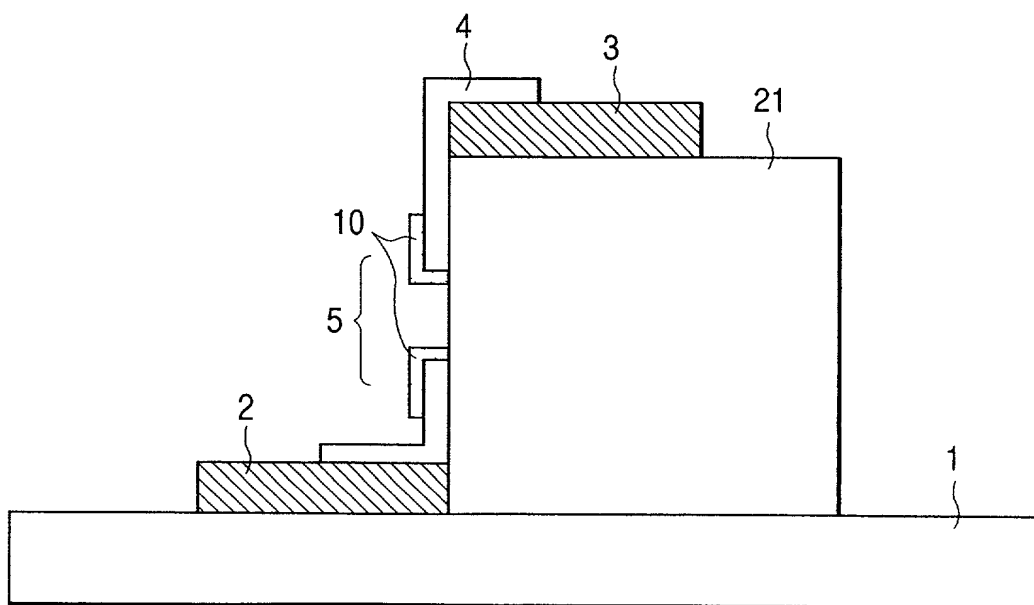


FIG. 22A

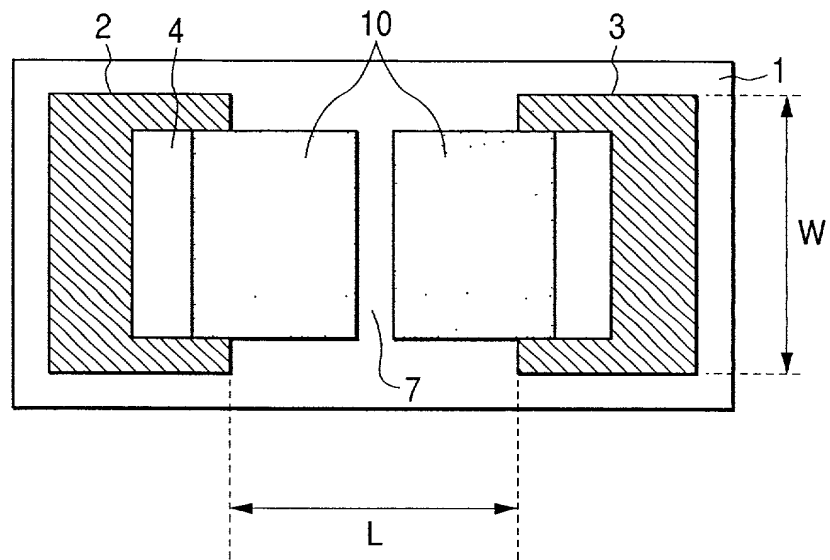
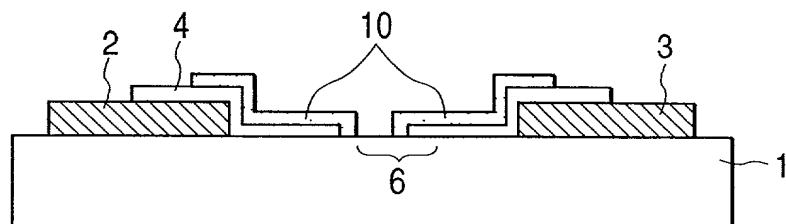


FIG. 22B



**COMBINED DECLARATION AND POWER OF ATTORNEY
FOR PATENT APPLICATION**
(Page 1)

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled _____

ELECTRON-EMITTING DEVICE, ELECTRON SOURCE USING
ELECTRON-EMITTING DEVICE, AND IMAGE FORMING APPARATUS

the specification of which ☒ is attached hereto ☐ was filed on _____
as United States Application No. or PCT International Application No. _____
and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR §1.56.

I hereby claim foreign priority benefits under 35 U.S.C. §119(a)-(d) or §365(b), of any foreign application(s) for patent or inventor's certificate, or §365(a) of any PCT international application which designates at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT international application having a filing date before that of the application on which priority is claimed:

Country	Application No.	Filed (Day/Mo./Yr.)	(Yes/No) Priority Claimed
Japan	10-348454	December 8, 1998	Yes
Japan	11-042908	February 22, 1999	Yes
Japan	11-319289	November 10, 1999	Yes

I hereby claim the benefit under 35 U.S.C. §120 of any United States application(s), or §365(c) of any PCT international application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of 35 U.S.C. §112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 C.F.R. §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

Application No.	Filed (Day/Mo./Yr.)	Status (Patented, Pending, Abandoned)
-----------------	---------------------	--

I hereby appoint the practitioners associated with the firm and Customer Number provided below to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith, and direct that all correspondence be addressed to the address associated with that Customer Number:

FITZPATRICK, CELLA, HARPER & SCINTO
Customer Number: 05514

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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COMBINED DECLARATION AND POWER OF ATTORNEY
FOR PATENT APPLICATION

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